



FBs - Series Programmable Logic Controller

- Cutting edge PLC
- State of the art technology
- Compact & Powerful
- Extensive product range
- Reliable & Durable

Since 1992



.....more than a decade of unsurpassed



"Quality" and "Functionality"

Contents

Features	01
System Configuration	03
General Specifications	05
Main Unit Specifications	
• Basic Main Units (MA)	09
• Basic Main Units (MA/MB)	09
• Advanced Main Units (MC)	09
NC Positioning Main Units (MN)	10
Right Side Expansion Module Specifications	
DIO Expansion Units	10
 Power Supplies for Expansion Modules 	11
DIO Expansion Modules	11
Thumbwheel Switch Module	11
• 16/7 Segment LED Display Modules	12
AIO Modules	12
Temperature Measurement Modules	12
• AI + Temperature Measurement Combo Modules	13
Voice Module	13
Load Cell Module	13
Potential Meter Module	13
Left Side Expansion Module Specifications	
 General Communication Boards/Modules 	13
Ethernet Communication Boards/Modules	14
CANopen [®] Communication Board	14
 ZigBee[™] Communication Modules 	14
 GSM Communication Module 	14
 General Purpose Communication Modules 	14
AIO Boards	15
 3-Axis Motion Control Module 	15
 Precision Load Cell Module 	15
 Handheld Programming Panel 	15
• Simple HMI	15
Peripheral and Accessory Specifications	
RFID Card	16
• PWMDA	16
Memory Pack	16
USB-RS232 Converter Cable	16
Communication Cable	16
 High Density DIO Connection Cable 	16
• 16/7 Segment LED Display	16
• Training Box	17
Program Development Software WinProladder	18
Instruction Sets	19
Dimensions	21
Model List	23

Features

SoC-FATEK's Core Technology

The FBs-PLC's design incorporates a "System on Chip" (SoC) developed in-house by Fatek Corporation. The BGA chip consists of over 120,000 gates which integrates powerful features such as a Central Processing Unit (CPU), Memory, Hardware Logic Solver (HLS), 5 high-speed communication ports, 4 sets of hardware high-speed counters/timers, 4 axes of high-speed pulse outputs for NC positioning control (with linear interpolation), 16 high-speed interrupts and captured inputs. The FBs-PLC represents high functionality and reliability with exceptional value compared to other PLC's in its class.



User friendly and powerful instruction sets

The FBs-PLC has more than 300 instructions which adopts a user friendly and readable multi-input/multi-output function structure. With this multi-input instruction structure the user can derive many types of functionality which other brands of PLC's may require the use of many instructions to achieve this. Also the operation result can be directly sent to internal or external outputs. To increase the program readability, the inputs or outputs for each function instruction have their own mnemonic symbol attached and the content of each operand is also displayed. For high-end applications, such as PLC networking (LINK), PID control and NC positioning etc, the FBs-PLC provides dedicated convenient instructions to assist in program development.

Communication function (up to 5 ports including RS232, RS485, USB, Ethernet, CANopen[®] and GSM and ZigBee[™] wireless communication)

Via the five high-speed communication ports included in the SoC, the FBs-PLC's communication capability is outstanding operating at a maximum speed of 921.6Kbps. Communications can be achieved using ASCII code or the double-speed binary code. Along with FATEK's standard protocol, Modbus ASCII/RTU/TCP or user-definable protocols are also available. The FBs-PLC also provides the option of 8 different communication boards and 10 different communication modules for various types of communication applications. With their high speed and functionality the FBs-PLC has the greatest number of communication ports than any other PLC in its class. Each communication port comes standard with LED indicators for transmission (TX) and reception (RX) to enable the user to monitor the operation.

Up to 4 sets of high-speed pulse width modulation (HSPWM) output

The SoC inside the FBs-PLC incorporates four sets of hardware high-speed pulse width modulation outputs with a maximum frequency of 184.32KHz and 18.432KHz with resolutions of 1% and 0.1%, respectively. Different from the PWM function operated by software alone in other brands of PLC's, the hardware driven high-speed PWM in the FBs-PLC provides the user with easy control with high precision and stability.

PLC & NC Control in one and Dedicated NC Positioning Language

NC Position Control is incorporated into the SoC of the FBs-PLC which integrates PLC+NC control into one unit in order for resources sharing and reducing the need of data exchange. The NC position control adopts special positioning command language, which allows programming by mechanical or electrical units and the changing control of parameters during execution. One single unit has up to four axes outputs with a maximum frequency of 200KHz (MC) or 920KHz (MN) and equipped with multi-axis linear interpolation function. If combined with the four sets of built-in HHSC, it can achieve a fully closed loop positioning control!

Integrated high-speed counters with counting frequency up to 920 KHz

The FBs-PLC includes up to 4 sets of hardware high-speed counters (HHSC) and 4 sets of software high-speed counters (SHSC). The highest counting frequency of a HHSC is 200KHz (MC) or 920KHz (MN). Each HHSC also has a clear and mask function. There are 8 counting modes including U/D, U/Dx2, P/R, P/Rx2, A/B, A/Bx2, A/Bx3 and A/Bx4 which makes the HHSC very powerful and efficient. For example, if the encoder, running at 200 pulses per revolution, adopts A/Bx4 mode the FBs-PLC can achieve the same result that 800 pulses per revolution encoder can provide. The counter is implemented in the hardware so as not to occupy CPU processing time. In addition, 4 sets of software high-speed counters (SHSC) has U/D, P/R, A/B 3 types of counting modes and the total counting frequency is 5KHz.

High-speed timers (HST)

The FBs-PLC is the only PLC in this class providing 0.1mS high-speed timers (the FBs-PLC having one 16-bit and 4 sets of 32-bit HST). Currently, the fastest time base of high speed timers used in other brands of PLC's is 1mS. By incorporating the interrupt function of the FBs-PLC the accuracy of 0.1mS time base high-speed timer of FBs-PLC is further enhanced and can easily achieve more precise speed detection or can be used as a frequency meter. In most cases, expensive speed detection equipment can be replaced by the economical FBs-PLC.

FATEK's Powerful Communication Features

The five communication ports in FBs-PLC can simultaneously connect to various intelligent peripherals with various interfaces such as USB, RS232, RS485, Ethernet, CANopen® and ZigBee[™]. Apart from the FATEK and Modbus protocol or communication through the FATEK communication server, the user can also use the PLC's CLINK instruction for user-defined protocol to actively or passively establish connections with many intelligent peripherals.



Open communication driver

The open communication protocol of the FBs-PLC is supported by all major brands of Supervisory Software (Scada) and Operator Terminals (HMI). Scada software such as Wonderware, Citec, Labview and LabLink! Operator terminals (HMI) such as Proface, Hitech/Beijer and Cermate can be directly connected with the FBs-PLC via serial and Ethernet interfaces. FATEK also provides FATEK DDE standard communication server or thirdparty OPC server for the user to easily connect the FBs-PLC to various control or supervisory systems. In addition, reputable companies such as National Instruments and KONTRON both sell FATEK OPC software package for users.

Complete range of peripherals

In addition to over 200 models of main CPU units, the FBs-PLC also provides about 100 models of expansion I/O for selection. The expansion I/O modules include basic DI/O, AI/O and other communication modules, also include thumbwheel switch input module, 16/7 segment LED display module, 8 types (J, K, R, S, E, T, B, N) thermocouple, Pt100, Pt1000 RTD temperature measurement modules. There is also a new additions to the range including load cell module used in weighting, potential meter module used in measuring position, and a user-friendly voice module. The FBs-PLC also provides a FBs-DAP or FBs- PEP simple HMI which can be linked together with a single RS485 bus. The FBs-DAP or FBs-PEP can be a simple Timer/Counter editor or it can also be used as a simple human machine interface through the function of user definable keys and message display. The FBs-DAP or FBs-PEP can be equipped with a wireless RFID sensing module and can be applied to such applications as entrance control, parking equipment and elevator control amongst others.

User-friendly operating environment

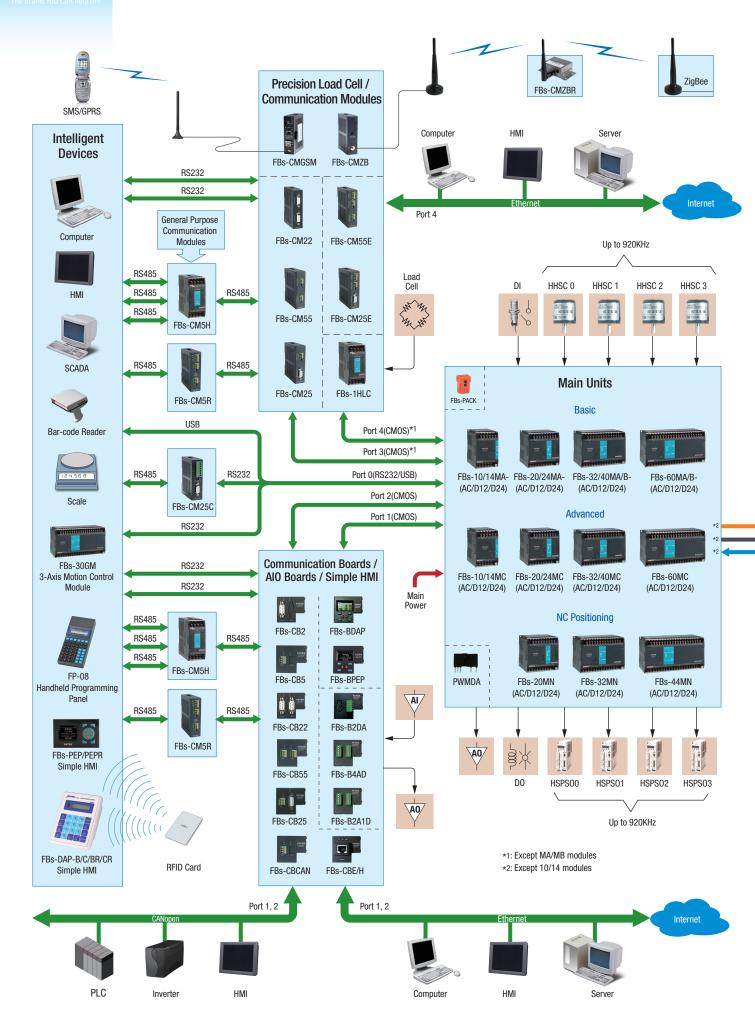
"WinProladder" is the Windows-based ladder diagram programming software for the FBs-PLC. It provides a user-friendly operating environment with editing, monitoring and debugging functions which allows the user to become familiar with the operation of the software in a very short time. The powerful editing function of WinProladder, assisted with keyboard, mouse and on-line help (of ladder instructions and operating guide) greatly reduces programming development time. Features which can display the data registers directly in the ladder diagram and provide multiple status pages for monitoring gives the user the ability to monitor and debug easily.

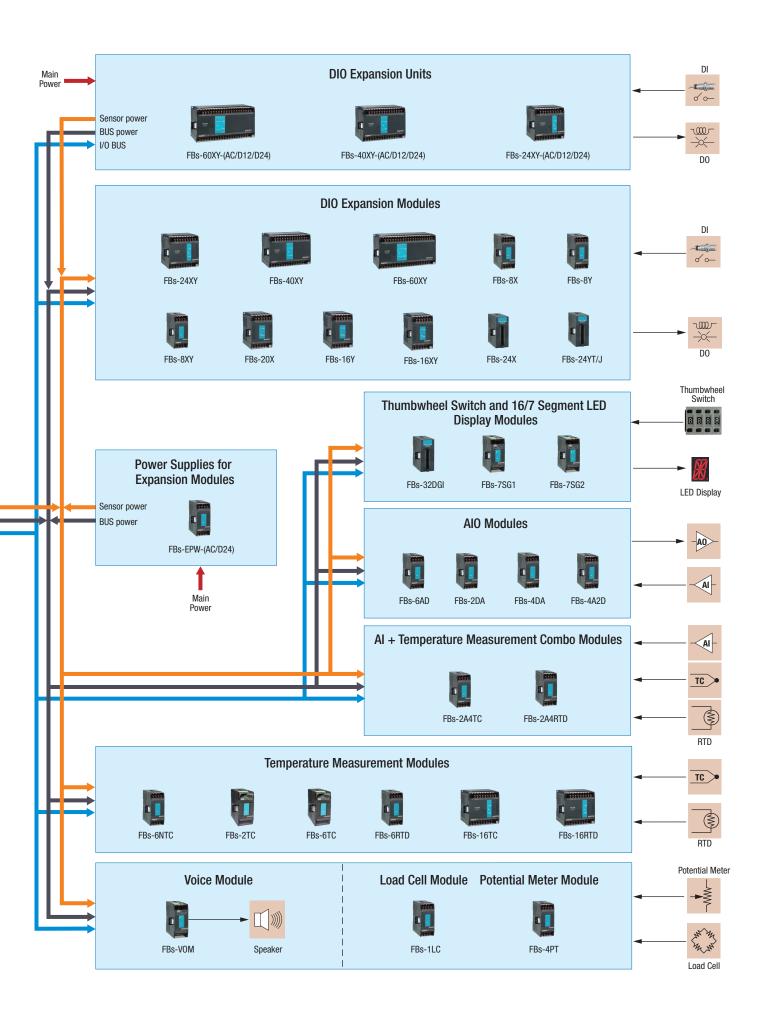
Up to 36 points of captured input

The SoC in the FBs-PLC has a captured input function, which captures and stores the external pulse of an input shorter than the scanning time of the CPU. Compared to PLC's in this class that either lack this capability or require highly sophisticated interrupt functions (which increase the CPU processing time), the FBs-PLC can handle this task easily as a general input, easily configured with high efficiency and no detriment the CPU scan time.

Single unit with 16 points of high-speed interrupt

The FBs-PLC provides 16 points of external interrupts. The interrupt is edge driven and the user can define which edge triggers the interrupt and can be positive, negative or both edges. The interrupts can perform high speed, emergency processing which can withstand the time jilter caused by the delay and deviation of the scan time and can be used for precision high speed positioning, machine home and high speed RPM measurement applications.





FATEK[®] The Brand You Can Re

General Specifications

Environmental specifications

	Item		Specification	Note		
	Enclosure	Minimum	5°C			
Operating	space	Maximum	40°C	Permanent installation		
ambient temperatur	e Open	Minimum	5°C	Permanent installation		
	space					
	Storage temperature		-25~70°C			
Relative	humidity(non-condensin	g, RH-2)	5~95%			
	Pollution resistance		Degree II			
	Corrosion resistance		Base on IEC-68 standard			
	Altitude		≤2000m			
Vibration	Fixed by DIN	RAIL	0.5G, 2 hours for each direction of 3 axes			
resistance	Fasten by so	rew	2G, 2 hours for each direction of 3 axes			
	Shock resistance		10G, three times for each direction of 3 axes			
	Noise resistance		1500 Vp-p, pulse width 1μS			
	Withstand voltage 1500VAC, 1 minute L, N to any t					

AC power supply specifications

Item		10/14 points main units	20/24 points main units	32/40 points main units	60 points main units			
Input rongo	Voltage		100~240VAC	2, -15%/+10%				
Input range	Frequency	50/60Hz ±5%						
Max. power consumption (bu	ilt-in power supply)	21W(SPW14-AC) 36W(SPW24-AC)						
Inrush curre	nt	20A@264VAC						
Allowable power momentary	y interruption time	< 20mS						
Fuse rating	9	2A, 250V						

DC power supply specifications

Item	10/14 points main units	20/24 points main units	32/40 points main units	60 points main units		
Input voltage		12 or 24 VDC, -15%/+20%				
Max. power consumption (@ full built-in power supply)	21W(SPW14-D12/D24)	2/D24) 36W(SPW24-D12/D24)				
Inrush current		20A@12 or	24VDC			
Allowable power momentary interruption time	< 2mS					
Fuse rating	3A(D12)/1.5A(D24),125V	5A(D12)/2.5A(D24),125V				

Main unit specifications

* : Default, changable by user

		Item	Specification	Note
	Execut	tion speed	0.33uS/Sequential instruction	
	Progra	m capacity	20K Words	
Program memory			FLASH ROM or SRAM + Lithium battery for Back-up	
	Sequenti	al instruction	36 instructions	
	Functior	ninstruction	326 instructions (126 kinds)	Include derivative instructions
Flo	w chart o	command (SFC)	4 instructions	
	Port 0 (RS232 or USB)		Communication speed 4.8k ~ 115.2Kbps (9.6Kbps)*	
Communication Interface	(RS232	Port 1 ~ Port 4 2, RS485 , Ethernet, CANopen or GSM)	Communication speed 4.8k ~ 921.6Kbps (9.6Kbps)*	Port1 ~ 4 provides FATEK or Modbus RTU/ASC II or user defined communication protocol
		Maximum link stations	254	
	Х	Input contact (DI)	X0~X255 (256)	Corresponding to external digital input
Digital (Bit status)	Y	Output relay (DO)	Y0~Y255 (256)	Corresponding to external digital output
	TR Temporary relay		TR0~TR39 (40)	

General Specifications

(Continue)

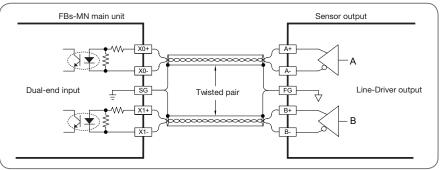
		Iten	n			Speci	fication		Note
					M0 ~ M799 (800)	*			Can be configured as retentive type
		Internal relay		Non-retentive	M1400 ~ M1911 ((512)			
Dic	M	-		Retentive	M800 ~ M1399 (6	500)*			Can be configured as non-retentive type
Digital (Bit status)		Special relay			M1912 ~ M2001	(90)			
(Bit				No	CO. C 400 (500)*				S20 ~ S499 can be configured as
sta	S	Step relay		Non-retentive	S0 ~ S499 (500)*				retentive type
tus)				Retentive	S500 ~ S999 (500))*			Can be configured as non-retentive type
	Т	Timer "Time-Up"	status c	ontact	T0 ~ T255 (256)				
	С	Counter "Count-L	Jp" statu	s contact	C0 ~ C255 (256)				
			0.01S T	Time base	T0 ~ T49 (50)*				
	TMR	Timer current	0.1S Ti	me base	T50 ~ T199 (150)	*			T0 ~ T255 numbers for each time base can
		value register	1S Tim	e hase	T200 ~ T255 (56)	*			be adjusted.
			10 1111						
			16-bit	Retentive	C0 ~ C139 (140)*				Can be configured as non-retentive type
	CTR	Counter current		Non-retentive	C140 ~ C199 (60)				Can be configured as retentive type
		value register	32-bit	Retentive	C200 ~ C239 (40)		Can be configured as non-retentive type		
				Non-retentive	C240 ~ C255 (16) R0 ~ R2999 (300)				Can be configured as retentive type Can be configured as non-retentive type
	HR			Retentive	D0 ~ D3999 (400	-			can be configured as non-retentive type
Reg	DR			Non-retentive	R3000 ~ R3839 (400	-	Can be configured as retentive type		
iste					13000 113035 (540)			When not configured as ROR, it can serve
r (Wo	HR	Data register		Retentive	R5000 ~ R8071 (3072)*			normal register (for read/write)
Register (Word data)	ROR			Read only register	R5000 ~ R8071 can be set as ROR ~ default setting is (0)*				ROR is stored in special ROR area and not occupy program space
1)		lanut venieter		File register	F0 ~ F8191 (8192		Save/retrieved via dedicated instruction		
	IR	Input register			R3840 ~ R3903 (Corresponding to external numeric input
	OR	Output register	aiotor		R3904 ~ R3967 (-	005 (02)		Corresponding to external numeric output
		Special system re 0.1mS high-spee		aiotor	R3968 ~ R4167 (1		095 (96)		
	SR			-	R4152 ~ R4154 (3 DR4096 ~ DR411				
	on	High-speed counter register		Hardware (4 sets) Software (4 sets)					
				5011Wale (4 Sels)	DR4112 ~ DR4120 R4128 (sec)	R4129 (min)	R4130 (hour)	R4131 (day)	
		Calendar Registe	r		R4128 (sec) R4132 (month)	R4129 (IIIII) R4133 (year)	R4130 (nour)	R4131 (uay)	Optional for MA model
	XR	Index register			V · Z (2), P0 ~ P9 (10)				
nterru		External interrupt	t control				sitive/negative ed	ae)	
control	JL	Internal interrupt			32 interrupts (16 points input positive/negative edge) 8 interrupts (1, 2, 3, 4, 5, 10, 50, 100mS)				
).1mS I	high spe	ed timer(HST)			1 (16-bit), 4 (32-b	oit, share with HH	ISC)		
			N	o. of channel	Up to 4				
Hi	Hardw	are high-speed co	unter C	ounting mode	8 modes (U/D, U,	/Dx2, P/R, P/Rx2,	A/B, A/Bx2, A/Bx3	3, A/Bx4)	
High-speed counter (HSC)) /32-bit			Maximum is 200	KHz (Single-end	input) or 920KHz		• Total number of HHSC and SHSC is 8 HHSC can be converted into 32-bit/0.1mS
(HS			C	ounting frequency	(differential inpu	it)	1 /		time base High-Speed Timer (HST)
C) c)			N	o. of channel	Up to 4				• Half of maximum frequency while A/B
unt		are high-speed cou	unter C	ounting mode	3 modes (U/D, P/	′R, A/B)			input
<u>a</u>	(SHSC) /32-bit		ounting frequency	Maximum sum u	ip to 5KHz			-
		Number of axis		5	Up to 4				
10						KHz (Single-end	output) or 920KH	Z	
IC lositior	ı	Output frequence	;y		(differential outp				Half of the maximum while A/B output
oulse o		Pulse output mo	ode		3 modes (U/D, P/	′R, A/B)			
HSPSO))	Programming m	ethod		Dedicated positi	on language			
		Interpolation			Maximum 4 axes	linear interpola	tion		
		Number of point	S		Up to 4				
ISPWN utput	Λ	Output frequenc	у		72Hz ~ 18.432KH 720Hz ~ 184.32K				
			Do	inte		•		able this footure	
			PO	ints	-		main unit are suit	able this feature)	
apture	ed input		Mi	nimum capturable	>10 µS (for ultra				
				lse width	>47 µS (for Medi				
				>470 µS (for Med	· · · ·				
	X0 ~ X15		Adjustable frequ				Chosen by frequency at high frequency		
Jigital	ital filter		Adjustable time constant 0 \sim 1.5mS/0 \sim 15mS (unit: 0.1mS/1mS)			Chosen by time constant at low frequency			
			X1	6 ~ X35	Time constant 1	~ 15mS adjustab	le (unit: 1ms)		

FATEK[®] The Brand You Can Rely ont General Specifications

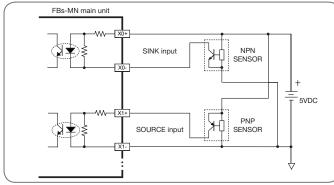
Digital Input (DI) Specifications

	Item	5VDC differential input	differential input 24VDC single-end input					
Specification		Ultra high speed	High speed	Medium speed(HSC)	Medium low speed (capture input)	Low speed	Notes	
Maximum input frequency*/ accumulated time		920KHz	200KHz	20KHz(HHSC) Total 5KHz(SHSC) 0.47mS 4.7mS		4.7mS		
Input signal voltage		5VDC ± 10%		24VE	DC ± 10%			
Threshold	ON	>11mA	>8mA	>4mA		>2.3mA	× 11 IC C .	
current	OFF	<2m/	Ą	<1.	.5mA	<0.9mA	*: Half of maximum frequency while A/B	
Maximum i	nput current	20mA	10.5mA	7.6	бmА	4.5mA	phase input	
Input ir	ndication							
Isolatio	n method		Photoco	ouple isolation, 500VAC,	1 minute			
SINK/SOL	IRCE wiring	Independent wiring	Via variatio	n of internal common te	rminal S/S and external co	ommon wiring		
Noise filtering methods		DHF (0~1 +AHF (0.4		DHF (0~15mS) +AHF (4.7μS)	DHF (0~15mS) +AHF (0.47mS)	AHF (4.7mS)	DHF: Digital Hardware Filter AHF: Analog Hardware Filter	

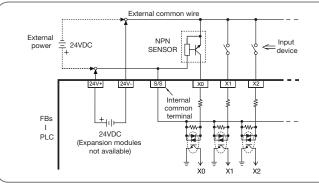
Wiring of 5VDC differential input (with frequency up to 920KHz, for high speed or high noise environments)



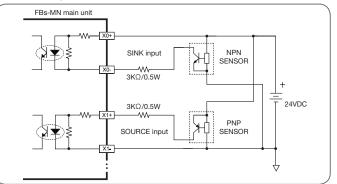
Wiring of 5VDC differential input to 5VDC single-end SINK /SOURCE input (Max. 200KHz)



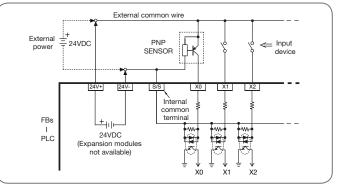
Wiring of 24VDC single-end SINK input



Wiring of 5VDC differential input to 24VDC single-end SINK /SOURCE input (Max. 200KHz)



Wiring of 24VDC single-end SOURCE input



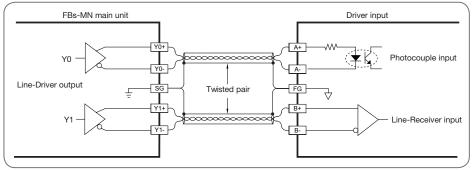
General Specifications

Digital Output (DO) Specifications

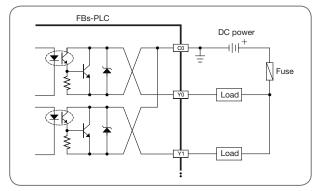
	Item	Differential output	Sin	gle-end transistor outp		Single-end		
Specification		Ultra high speed	High speed	Medium speed	Low speed	relay output		
Maximun	n output frequency*	920KHz	200KHz 20KHz —		200KHz 20KHz —		—	-
Wo	orking voltage	5VDC±10%		< 250VAC/30VDC				
Maximum load	Resistive	50mA	0.5A	0.5A	0.5A/0.1A (24YT/J)	2A/single, 4A/common		
current	Inductive	JUIIA	0.3A	0.54	0.3A/0.1A (2411/3)	80VA(AC)/24VA(DC)		
Maximum voltage drop/ conducting resistan		_	0.6V 2.2V		2.2V	0.06V (initial)		
Minimum load		_		2mA/DC power				
Lea	akage current	_		-				
Maximum output	ON→OFF	200nS	2µS	15	- 10mS			
delay time	OFF→ON	200115	ΖμΟ	30	10115			
Output	t status indication		Displayed by LE	D: Light when "ON", dar	k when "OFF"			
Over c	urrent protection			N/A				
ls	solation type		Electromagnetic isolation 1500VAC, 1 minute					
SINK/SOURCE output type		Independent dual terminals for arbitrary connection	Choo	Can be arbitrarily set to SINK/SOURCE output				

 * : Half of the maximum frequency while A/B phase output

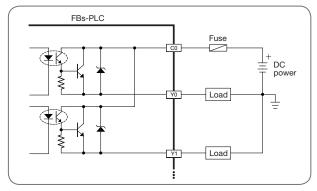
Wiring of 5VDC differential output (with frequency up to 920KHz, for high speed or high noise environments)



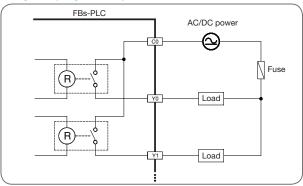
Wiring of transistor single-end SINK output



Wiring of transistor single-end SOURCE output



Wiring of relay single-end output





FATEK The Brand You Can Rely ont Main Unit Specifications

Basic	: Main Uni	ts (MA)									
Specifi	ication	Model	FBs-10MAR	FBs-10MAT/J	FBs-14MAR	FBs-14MAT/J	FBs-20MAR	FBs-20MAT/J	FBs-24MAR	FBs-24MAT/J	
Digital Input	24VDC	Medium speed (20KHz)		4 pc	pints		6 pc	6 points 8 points			
	24VDC	Medium speed (Total 5KHz)	2 pc	oints	4 pc	pints		6 pc	oints		
Digital	Relay		4 points	_	6 points	_	8 points	_	10 points	_	
ital output	Transistor	Medium speed (20KHz)		4 points		6 points		8 points		8 points	
put		Low speed		_		—				2 points	
Comn	nunication	Built-in	1 port (Port0, USB or RS232)								
	Port	Expandable	2 ports (Port1~2, RS485 or RS232 or Ethernet)								
	Cal	endar		optional							
	Built-in po	ower supply		SPW14-AC	C/D12/D24			SPW24-AC	C/D12/D24		
	Wiring m	nechanism	7.62mm fixed terminal block								
	Dime	ension		Figure 2 Fi				Figu	ire 1		

Basic Main Units (MA/MB)				

Specification		Model	FBs-32MAR FBs-32MBR	FBs-32MAT/J FBs-32MBT/J	FBs-40MAR FBs-40MBR	FBs-40MAT/J FBs-40MBT/J	FBs-60MAR FBs-60MBR	FBs-60MAT/J FBs-60MBT/J			
Digit		Medium speed (20KHz)		8 points							
Digital Input	24VDC	Medium speed (Total 5KHz)		8 points							
1		Medium low speed	4 pc	pints	8 pc	pints	20 p	oints			
Dig		Relay	12 points		16 points	—	24 points	—			
Digital ou	Transistor	Medium speed (20KHz)	_	8 points	_	8 points	_	8 points			
output		Low speed	—	4 points		8 points	_	16 points			
Con	nmunication	Built-in	1 port (Port0, USB or RS232)								
	Port	Expandable			2 ports (Port1~2, RS48	5 or RS232 or Ethernet)				
	Са	lendar			opti	ional					
	Built-in p	ower supply			SPW24-AG	C/D12/D24					
	Wiring	mechanism		7.62mm fixed	terminal block(MA), 7.6	2mm detachable term	inal block (MB)				
	Din	nension			Figu	ure 1					



Spec	ification	Model	FBs-10MCR	FBs-10MCT/J	FBs-14MCR	FBs-14MCT/J	FBs-20MCR	FBs-20MCT/J	FBs-24MCR	FBs-24MCT/J	
Diç		High speed (200KHz)		2 po	ints		4 points				
Digital Input	24VDC	Medium speed (20KHz)		2 po	ints		2 points		4 points		
out		Medium speed (Total 5KHz)	2 pc	pints	4 pc	pints		6 points			
	Relay		4 points	—	6 points	_	8 points	—	10 points	_	
Digital	Transistor	High speed (200KHz)	_	2 points	—	2 points		4 points	—	4 points	
output		Medium speed (20KHz)	_	2 points	—	4 points	_	4 points	—	4 points	
		Low speed	—	—	—	_	_	—	—	2 points	
Com	nunication	Built-in				1 port (Port0,	USB or RS232)				
	Port	Expandable			4 ports (Port1~	-4, RS485 or RS23	2 or Ethernet or	GSM or ZigBee)			
	С	alendar				Bui	lt-in				
	Built-in	power supply		SPW14-AC	/D12/D24		SPW24-AC/D12/D24				
	Wiring	mechanism		7.62mm fixed t	erminal block		7.62mm detachable terminal block				
	Di	mension		Figu	re 2			Figu	ire 1		

Advanced Main Units (MC)

Main Unit Specifications

Adv	vanced Ma	in Units (MC)							
Spe	cification	Model	FBs-32MCR	FBs-32MCT/J	FBs-40MCR	FBs-40MCT/J	FBs-60MCR	FBs-60MCT/J	
		High speed (200KHz)		6 рс	pints		8 pc	pints	
Digital Input	24VDC	Medium speed (20KHz)		2 pc	vints		-	_	
Input	24VD6	Medium speed (Total 5KHz)							
		Medium low speed (0.47ms)	4 pc	pints	8 pc	pints	20 points		
		Relay	12 points	—	16 points	—	24 points	—	
Digital		High speed (200KHz)	—	6 points	—	6 points	_	8 points	
output	Transistor	Medium speed (20KHz)		2 points	_	2 points			
1		Low speed	_	4 points		8 points	—	16 points	
Cor	nmunication	Built-in	1 port (Port0, USB or RS232)						
	Port	Expandable	4 ports (Port1~4, RS485 or RS232 or Ethernet or GSM or ZigBee)						
	Cale	endar			Bui	lt-in			
	Built-in po	ower supply			SPW24-AC	C/D12/D24			
	Wiring m	echanism			7.62mm detachat	ole terminal block			
	Dime	ension			Figu	ire 1			



		J ()						
Sp	ecification	Model	FBs-20MNR	FBs-20MNT/J	FBs-32MNR	FBs-32MNT/J	FBs-44MNR	FBs-44MNT/J
Di	5VDC Differential	Ultra high speed (920KHz)	2 points (1 axis)		4 points	4 points(2 axes)		s(4 axes)
Digital		High speed (200KHz)	4 pe	pints	4 pc	pints		_
Input	24VDC	Medium speed (Total 5KHz)	6 p	oints		8 pc	pints	
		Low speed	—		4 pc	pints	12 p	ooints
		Relay	6 points	_	8 points	—	8 points	_
Digital (5VDC Differential	Ultra high speed (920KHz)	2 points (1 axis)		4 points (2 axes)		8 points(4 axes)	
output	Transister	High speed (200KHz)	—	6 points	_	4 points	—	_
17	Transistor	Low speed	—	_	_	4 points	—	8 points
Co	mmunication	Built-in	1 port (Port0, USB or RS232)					
	Port	Expandable	4 ports (Port1~4, RS485 or RS232 or Ethernet or GSM or ZigBee)					
	С	alendar	Built-in					
	Built-in	power supply	SPW24-AC/D12/D24					
	Wiring	mechanism	7.62mm detachable terminal block					
	Di	mension			Figu	ire 1		

Right Side Expansion Module Specifications

NC Positioning Main Units (MN)

DIO Expansion Units								
Specific	ation	Model	FBs-24XYR	FBs-24XYT/J	FBs-40XYR	FBs-40XYT/J	FBs-60XYR	FBs-60XYT/J
Digital Input	24VDC Low speed		14 p	oints	24 p	oints	36 p	ooints
Digital output		Relay	10 points		16 points		24 points	_
Digital output	Transistor	Low speed	_	10 points	_	16 points	_	24 points
	Built-in pow	er supply	SPW24-AC/D12/D24					
	Wiring mee	chanism	7.62mm fixed terminal block					
	Dimen	sion			Figu	ıre 1		

Right Side Expansion Module Specifications

Power Supplies for Expansion Modules					
Specifi	cation Model	FBs-EPW-AC	FBs-EPW-D24		
Capac output	5VDC Bus power	40	0mA		
Capacity output po	24VDC Bus power	25	0mA		
city of power	24VDC Sensor power	25	50mA		
	Input voltage	100~240 VAC, -15%/+10%	24VDC, -15%/+20%		
	Maximum power consumption	21W			
V	Viring mechanism	7.62mm fixed terminal block			
	Dimension	Fig	ure 4		

DIO Expansion Modules										
Specific	ation	Model	FBs-8XYR	FBs-8XYT/J	FBs-8X	FBs-8YR	FBs-8YT/J	FBs-16XYR	FBs-16XYT/J	FBs-20X
Digital Input	24VDC	Low Speed	4 pc	pints	8 points	_	—	8 pc	pints	20 points
Digital	al Relay		4 points	_	_	8 points	_	8 points	_	—
Output	Transistor	Low Speed		4 points		_	8 points		8 points	_
V	Wiring mechanism					7.62 mm fixed	terminal block			
	Dimension				Figure 4				Figure 3	

(Contini	ue)				-	-			
Specific	cation	Model	FBs-16YR	FBs-16YT/J	FBs-24X	FBs-24YT/J	FBs-24XYR	FBs-24XYT/J	FBs-40XYR
Digital Input	24VDC	Low Speed			24 points	_	14 p	oints	24 points
	Relay		16 points	_	_		10 points	_	16 points
Digital Output	High dens	ity low speed				24 points			—
output	Transistor	Low Speed		16 points				10 points	
Wiring mechanism		7.62 mm fixed	terminal block	30 pins head	ler with latch	7.62	2 mm fixed terminal b	lock	
Dimension		Figu	ıre 3	Figu	ire 6		Figure 1		

24 points



7.62 mm fixed terminal block

Figure 1

16 points

annin a

mm

Thumbwheel Switch Module

...........



FBs-32DGI
10mS max.
8 words (32 digits/128 individual points)
1/8 duty multiplexing input scan
30 pins header with latch
Figure 6

Digital

Output | Transistor

Wiring mechanism Dimension

Low

Speed

Right Side Expansion Module Specifications





16/7 Segment LED Display Modules

10,7 Segment LED Display				
ion	Model	FBs-7SG1	FBs-7SG2	
Decoding display		•	ent a character. rracter including 0 ~ 9, -, E, H, c, t and blank	
Non-dec	oding display		s needs 8 bits to control (including decimal), displayable any set of nber display) or each LED display	
		1 channel, 7 segment 8 words / 16 segment 4 words or 64 points individual LED	2 channels, 7 segment 16 words/ 16 segment 8 words or 128 points individual LED	
sh time fo	or display	10mS	max.	
Drivir	ig current	40mA / segment		
Displa	ay method	1~8 duty multiplexing display		
Driving	Low voltage	5VDC (can be 10% up)		
voltage	High voltage	7.5V, 10V, 12.5V selectable (can be 10% up)		
		0.6V, 1.2V, 1.8	3V selectable	
age drivir	ng indication	Each channel has individual Over Voltage (O.V.) driving LED indication (should be under Test Mode)		
plation m	ethod	Transformer (power) and photocouple (signal) isolation, 500VAC, 1 minute		
ver consu	mption	24VDC-15%/+20%, static consumption is 2W max., dynamic current is increased according to display		
ring mech	anism	16 pins flat cable, 2.54	mm header connector	
Dimensi	on	Figu	ire 4	
	on Decod Non-deco number o (points th time fo Driving Driving Voltage Fine tun age drivir Ilation mo er consu ing mech	Model Decoding display Decoding display Non-decoding display number of character (points) th time for display Driving current Display method Driving Low voltage	Model on FBs-7SG1 Decoding display 4 bits to repress It can display 16 kinds of pre-decoded cha segments (EX: character and nur segments (EX: character and nur segments (EX: character and nur rumber of character (points) number of character (points) 1 channel, 7 segment 8 words / 16 segment 4 words or 64 points individual LED h time for display 10mS Driving current 40mA / 1 0 mS Display method 1~8 duty multi Driving Low voltage High voltage 5VDC (can 0.6V, 1.2V, 1.2SV selec Fine tune of voltage drop 0.6V, 1.2V, 1.2SV selec Ged driving indication Each channel has individual Over Voltage (O.V) du Ialtion method Transformer (power) and photocoup er consumption 24VDC-15%/+20%, static consumption is 2W max. 16 pins flat cable, 2.54	

AIO Module							
Specification	Model	FBs-6AD	FBs-4A2D	FBs-2DA	FBs-4DA		
Input	point	6 points	4 points	—	—		
Outpu	t point	—	2 points	2 points	4 points		
Input/Out	put value		-8192~8191 or 0	0~16383 (14-bit)			
Input/output	Bipolar		Voltage: -10~10V or -5~5V Cu	rrent: -20~20mA or -10~10mA			
Signal range	Unipolar		Voltage: 0~10V or 0~5V Current: 0~20mA or 0~10mA				
Maximum	resolution	Voltage: 0.3mV (5V/16384) Current: 0.61µA (10mA/16384)					
Accu	iracy	± 1%					
Convers	ion time	Conversion once for each scan					
Maximum i	nput signal	Input voltage: ±15V I	nput current: ±30mA	—			
Allowable	load range	—	Output v	voltage: $500\Omega \sim 1M\Omega$ Output current: $0 \sim 500\Omega$			
Input im	pedance	Input voltage: 63.2K	Ω Input current: 250Ω				
Isolation	method	Transformer(power) and photocouple(signal) isolation, 500VAC, 1 minute, no isolation between each channel					
Power cor	nsumption		24VDC -15%/+2	20%, 3.2W max.			
Wiring me	echanism		7.62 mm fixed	terminal block			
Dime	nsion		Figu	ire 4			

Temperature Measurement Modules

-		
18151		











FBs-2TC FBs-6RTD FBs-16RTD FBs-6NTC FBs-6TC Specification Number of input points 2 points 6 points 16 points 6 points 16 points 6 points Thermocouple Sensor: Sensor type and J (-200~1200°C) E (-190~1000°C) 3-wire RTD sensor (JIS or DIN) NTC sensor temperature measurement K (-190~1300°C) T (-190~380°C) Pt100(-200~850°C) 10 KΩ at 25°C, B Pt1000(-200~600°C) R (0~1800°C) B (350~1800°C) optional -20~100°C range S (0~1700°C) N (-200~1000°C) Temperature compensation Built-in cold junction compensation Resolution 0.1°C Temperature refresh time 1 or 2 seconds 2 or 4 seconds 3 or 6 seconds 1 or 2 seconds 2 or 4 seconds 2 or 4 seconds **Overall Precision** ± (1%+1°C) ±1% of full scale at 25°C $\pm 1\%$ Transformer(power) and photocouple(signal) isolation, 500VAC, 1 minute, Transformer(power) and photocouple(signal) isolation, 500VAC, 1 minute, Isolation method isolation between each channel no isolation between each channel 24VDC -15%/+20%, 2W max. Power consumption Wiring mechanism 3.81 mm european terminal block 7.62 mm fixed terminal block Dimension Figure 4 Figure 1 Figure 4 Figure 1 Figure 4

Right/Left Side Expansion Module Specifications

Al+Temperature Measurement Combo Modules			
Specification Model	FBs-2A4TC	FBs-2A4RTD	
Analog input (AI) points	2 points	/ 14-bit	
Temperature measurement input points	4 points (thermocouple)	4 points (RTD)	
Analog input specification	Same as FBs-6AD	Same as FBs-6AD	
Temperature input specification	Same as FBs-6TC	Same as FBs-6RTD	
Power consumption	24VDC-15%/+20%, 2W max.		
Wiring mechanism	7.62 mm fixed terminal block		
Dimension	Figu	re 4	

Load Cell Module



Specification Model	FBs-1LC
Number of channel	1 channel
Resolution	16-bit (including sign bit)
Occupied I/O points	1 IR (input register) and 8 points DO
Sampling frequency	5/10/20/25/60/120/240/480 Hz optional
Non-linearity degree	0.01% full scale @25 °C
Zero drift	0.2 μV/ °C
Gain drift	10 ppm/ ℃
Excitation voltage	5V, maximum load is 250Ω
Level of sensitivity	2mV/V, 5mV/V, 10mV/V, 20mV/V
Filters	Moving averages
Isolation method	Transformer (power) and photocouple (signal) isolation, 500VAC, 1 minute
Power consumption	24VDC, -15%/+20%, 2W
Wiring mechanism	7.62 mm fixed terminal block
Dimension	Figure 4

Left Side Expansion Module Specifications







Mode FBs-VON Specification Number of recorded messages 245 messages Sound storage device Internal memory or external SD memory card 1MB, can play up to 2 minutes of sound Internal memory Maximum recordings. sound storage External SD Maximum 4 GB memory card, up to 8000 capacity minutes of sound recordings can be played. memory card Applicable sound encoding format Mono 8 bit 8KHz sample Signal output Dual output 8Vp-p, 4Ω load 2W output Computer editing, SD memory card Sound input method Sound playback control PLC control or manual sequencing (test play) Volume control PLC control, total of 10 volumes I/O points occupy 8 points DI and 8 points DO Status display 3 LEDs Internal 5V, 500mA (@2W output) Power consumption Dimension Figure 4

Potential Meter Module

i otentiai meter m	iounic promo
Specification Model	FBs-4PT
Number of channel	4 channels
Resolution	14 or 12 bits
Occupied I/O points	4 IR (input registers) and 1 unused OR (output register)
Conversion time	Conversion once for each scan
Accuracy	±1%
Potential meter impedance	1Κ~10ΚΩ
Voltage Input Range	0~10V
Potential meter voltage	10V
Filters	Moving averages
Isolation method	Transformer (power) and photocouple (signal) isolation, 500VAC, 1 minute
Power consumption	24VDC, -15%/+20%, 2W
Wiring mechanism	7.62 mm fixed terminal block
Dimension	Figure 4

General Communication Boards/Modules	on 🌒 🗋 📩		Constant and Const		
Specification Model	FBs-CB2 FBs-CB2 FBs-CB5		FBs-CB5	FBs-CB55	FBs-CB25
RS232 Port	1 port (Port2)	2 ports (Port1, Port 2)	—	—	1 port (Port1)
RS485 Port	—	_	1 port (Port2)	2 ports (Port1, Port 2)	1 port (Port2)
Indicators	Each Port has its own TX, RX LED indicators				
Wiring mechanism	DB9F	DB9F 3 pins spring terminal DB9F, 3 pins sp		DB9F, 3 pins spring terminal	
Installation position	Expansion slot of main unit				

(Continue)			
Specification Model	FBs-CM22	FBs-CM55	FBs-CM25
RS232 Port	2 ports (Port3, Port4)	—	1 port (Port3)
RS485 Port	—	2 ports (Port3, Port4)	1 port (Port4)
Indicators	Each Port has its own TX, RX LED indicators		
Wiring mechanism	DB9F	3 pins spring terminal	DB9F, 3 pins spring terminal
Installation position		Figure 5	



Left Side Expansion Module Specifications

Ethernet Communica Boards/Modules	tion			
Specification Model	FBs-CBEH	FBs-CBE	FBs-CM25E	FBs-CM55E
Network interface	10/100 Base T		10 Base T	
Network protocol		TCP/UDP/IF	P, ICMP, ARP	
Application protocol	FATEK client and server mode, Modbus-TCP client or server mode		rver mode	
PLC interface	Port1, Port2		Port4	
PLC communication speed	115.2 Kbps		9.6K / 19.2K / 38.4K / 57.6K / 115.2Kbps / 230.4Kbps	
Expansion communication interface	N/A		RS232 (Port3), RS485 (Port4)	RS485 (Port3, Port4)
Application IP port number	FATEK port number 500, Modbus-TCP 502 or customized			
Security protection	IP based access control			
Indicators	Internet RX, TX, LINK LEDs indicators			
Wiring mechanism	RJ-45		DB9F, spring terminal block 4-pin x1, 3-pin x1	Spring terminal block 4-pin x1, 3-pin x1
Dimension (Installation position)	Expansion slot of main unit		Figu	ire 5

CANopen[®] Communication Board



Model	FBs-CBCAN
Communication standard	CAN 2.0A CANopen
Network topology	3-Phase fieldbus
Communication speed	10K / 20K / 50K / 125K / 250K / 500K / 1Mbps
Maximum number of connection station	127 stations
Method of sending signal	Event or cyclic transmission
Isolation method	Photocouple (signal) isolation, 500VAC, 1 minute
Number of PDO communication	RXPDO-10, TXPDO-10 total up to 80 registers
Number of SDO channels	Client -1, Server-1
Error control	Heartbeat
Wiring mechanism	3-pin spring terminal block
ID setup method	Same as PLC station number or setup by software
Working mode	Master or slave dual modes
Installation position	Expansion slot of main unit

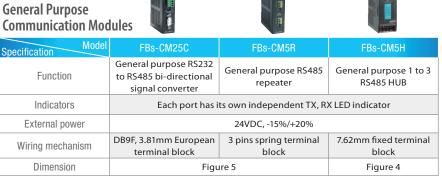
ZigBee™ Communication Modules

communication mouule			
Specification Model	FBs-CMZB	FBs-CMZBR	
Standards	Based on IEEE 802.15.4 and ZigBee™ standard		
Network topology	Mesh, Star, an	d Cluster-tree	
Frequency	2.4GHz, Unlice	nsed ISM Band	
Modulation	QP	SK	
Data rate	250 Kbps		
RF channels	16(5MHz)		
Data encryption	AES(option)		
Transmit power	-7~18dBm		
Transmission distance	1200m (LOS)		
Nodes	Maximum 65535		
Communication interface	Port3	—	
Power consumption	24VDC, -159	%/+20%, 2W	
Dimension	Figure 5	62 x 54 x 29 (mm)	

GSM Communication Module



Specification Model	FBs-CMGSM
Function	SMS, GPRS, and dial up data transfer (CSD), and etc
Frequencies	850/900/1800/1900MHz
RF power	2W
Communication interface	Port3
Dimension	Figure 5



Left Side Expansion Module Specifications

AIO Boards			
Specification Model	FBs-B2DA	FBs-B4AD	FBs-B2A1D
Input point	—	4 points	2 points
Output point	2 points	_	1 point
Input / Output value	0~1630 (14-bit representation, valid 12-bit)		
Input / Output polar	Unipolar		
Input / Output counting range	0~10V		
Conversion time	Conversion once for each scan		
Accuracy	±1%		
Isolation method	Non-isolation		
Wiring mechanism		3.81 mm European terminal block	
Installation position	The expansion slot of main unit		

3-Axis Motion Control Module

• • • • • • • • • • • • • • • • • • • •	
Specification Model	FBs-30GM
Number of DIO points	14 points (8 inputs/6 outputs)
Program capacity	16M Bytes
Data Register	20K Words
High speed pulse Input	200KHz X,Y,Z 3-Axis A/B differential signal input
High speed pulse Output	500KHz X,Y,Z 3-Axis A/B differential signal output
Manual input	A/B differential signal input
Communication port	RS485 x1, Ethernet x1
Built-in power supply	SPW24-AC/D12/D24
Wiring mechanism	7.62mm detachable terminal block
Dimension	Figure 1

Precision Load Cell Module

Simple HMI

1	-	
	177	
	-	

Specification Model	FBs-1HLC	
Number of channels	1 channel	
Resolution	0.10 μV/1D (24-bit AD)	
Filters	Digital filter, sampling rate 6.25~120Hz	
Measurement range	-1~39mV	
Sensor voltage	5VDC±5%	
No. of sensor connections	350Ω sensor x 8	
Isolation Method	Transformer (power) and photocouple (signal) isolation, 500VAC, 1 minute	
Power consumption	24VDC, -15%/+20%, 2W	
Wiring mechanism	7.62mm fixed terminal block	
Dimension	Figure 4	

Handheld Programming Panel

Specification Model	FP-08
Main function	Program editor (Mnemonic language), status monitoring, parameters setup, program/parameter import and recording, etc.
Max. of power consumption	5V/100mA
Keyboard	48 silicon rubber keys
Display	Two rows 16 characters, dot matrix LCD display, with LED backlight
Recording device	FBs-PACK read/write
Communication port	RS232 serial communication port
Connectors	DB9F, Mini-DIN
Dimension	Figure 7











Specification Model		FBs-DAP-B/BR	FBs-DAP-C/CR	FBs-PEP/PEPR	FBs-BDAP	FBs-BPEP		
	Display	Two rows 16-character, dot matrix LCD display, with LED backlighting		128x96 points white light OLED	128segments fixed-pattern LCD	128x64 points white light OLED		
	Key pads	20 buttons (4x	5) membrane	8 operation keys (rubber)	6 operation keys (rubber)	6 operation keys(rubber)		
Maxim	um of consumption power	24V, 48mA	5V, 120mA	5V, 100mA	5V, 100mA	5V, 100mA		
Cor	Electric	RS485	RS232	RS232	Port1, CMOS	Port1, CMOS		
Communication interface	Mechanism	5 pins European detachable terminal block DB9M		Mini-DIN	_	_		
ation ;e	Number of linked station	per of linked station Max. 16 stations Single unit Sir		Single unit	_	—		
	General features	Timer, counter, register, relay, access of contact in PLC						
	Special features	Alarm, information display, a ke	nd user definable special hot ys	finable special hot Station number setup, run/stop, Control Calendar* display and se				
Card access features (RFID card)		Available only in	-R models, with maximum dis	stance of 6~12cm				
Dimen	ision (Installation position)	Fig	ure 8	Figure 9	Expansion sl	Expansion slot of main unit		
The PLC main unit must be of calendar built-in type								

* The PLC main unit must be of calendar built-in type

Peripheral and Accessory Specifications

RFID Card			
Model Specification	CARD-H		
Operated frequency	13.56MHz		
Memory	64-bit with Cyclic Redundancy Check (CRC) on dat		
Working temperature	-25~50 (ISO7810)		
Power source	Powered by RF		
Receivable distance	6~12cm		
Writable times	At least 10000 times		

PWMDA	PPANDA Bola
Model Specification	PWMDA
Output range	0~10V
Output value	0~1000
Resolution	10mV(10V/1000)
Output impedance	1ΚΩ
Min. load(≥10V)	5.2KΩ
D/A conversion time	<50mS

Memory Pack	8	USB-RS232 Converter Cable				
Specification	FBs-PACK	Specification Model	FBs-U2C-MD-180			
Memory	1M bits FLASH ROM		Standard USB AM connector to RS232 MD4M connector (used in standard PC USB to FBs main unit Port 0 RS232), length 180cm			
Memory capacity	20K Words program + 20K Words data	Features				
Write protection	DIP switch ON/OFF protection					

Communication Cable				
Specification Model	FBs-232P0-9F-150	FBs-232P0-9M-400	FBs-232P0-MD-200	FBs-232P0-MDR-200
Features	Dedicated communication cable for FBs main unit Port 0 (RS232) to DB9F connector, length 150cm	Dedicated communication cable for FBs main unit Port 0 (RS232) to DB9M connector, length 400cm	Dedicated communication cable for FBs main unit Port 0 (RS232) to FBs-PEP/PEPR Mini-DIN male connector, length 200cm	Dedicated communication cable for FBs main unit port 0 (RS232) to FBs-PEP/PEPR 90 Mini-DIN male connector, length 200cm

High Density DIO Connection Cable		16/7 Segment LED B B B Display		XXXXX	
Specification Model	HD30-22AWG-200		DBAN.8-nR	DBAN2.3-nR	
Features	22AWG I/O cable with 30 pins Socket, length 200 (for FBs-24X, 24YT/J and 32DGI)	Features	0.8" 4-digit 16-segment LED display, , n means R(Red) 16-segment LED characters display installed, can be 1~4	2.3" 4-digit 16-segment LED display, n means R(Red) 16-segment LED characters display installed, can be 1~4	

(Continue)		8888888	<u>8.8.8.8</u> .
DB.56-nR	DB.8-nR	DB2.3-nR	DB4.0-nR
0.56" 8-digit 7-segment display, n means R(Red) 7-segment LED characters display installed, can be 1~8	0.8" 8-digit 7-segment display, n means R(Red) 7-segment LED characters display installed, can be 1~8	2.3" 8-digit 7-segment display, n means R(Red) 7-segment LED characters display installed, can be 1~8	4.0" 4-digit 7-segment display, n means R(Red) 7-segment LED characters display installed, can be 1~4

Training Box

Training Box

Specification	Model	FBs-TB0X				
	Case	Aluminum suitcase. Dimension is 46x32x16cm. Top cover and box body can be separated.				
Pov	wer supply	100~240VAC / 2A fuse / power switch with indicator				
	PLC	FBs-24MCT(transistor output)+FBs-CM25E(Ethernet communication module)				
	Programmer	FP-08 handheld programming panel, can develop program, monitor (optional)				
Programming tool	Winproladder		Instructor site: WinProladder with ' teaching assistant' utility			
1001	Programming Software		Student site: WinProladder			
	Built-in	Port0	RS 232 Mini-DIN			
	Communication	Port1				
Communication	board(CB) (optional)	Port2	RS232 or RS485 selectable, directly mounted on FBs-24MCT main unit			
interface		Port3	RS232, standard DB-9F connector			
	FBs-CM25E	Port4	RS485, 3-pin European terminal block			
		(Port4) Ethernet 10 Base T, IEEE 802.3 standard. Use port4 to interface PLC main unit				
Inpu	ut interface	Banana terminal and simulation switch with automatic and manual reset functions				
Outp	out interface	Banana terminal, 10 points. Transistor output(Y0~Y9). All outputs buffer with discrete relay before come to terminal. Y0 and Y1 also provide a direct output terminal for high-speed pulse output (HSPSO) application.				
Expansion	module (optional)	Secured by DIN Rail, 12.5cm wide slot, can accommodate three 4cm thin modules or other modules with equivalent width				
	Display module		4 digits 7-segment display module, attached with BCD decoding circuit			
	Thumbwheel switch		4 digits BCD thumbwheel switch module			
Application	Keyboard module		4 x 4 matrix keyboard module (Wiring coordinate with convenient instruction)			
peripheral	Encoder		Power supply 24VDC, 200P/R, open collector, A/B phase			
	Stepping motor		Pules/DIR control, 200P/R			
	LED display	10 c	f 10mmØ high-brightness LED (in red, yellow, and green), driven individually by Y0 to Y9			
Number of	of linked stations		Maximum 254 stations (1 station for instructor, 253 stations for student)			

Features:

- It contains the basic items required by PLC digital I/O training, such as the FBs-24MCT advanced main unit, the FBs-CM25E Ethernet module, digital input socket, simulated switches, and digital output socket.
- The built-in RS232, RS485 and the Ethernet three ports (can be expanded to five with communication boards) not only enable the teacher's computer to connect with the training kits of all students to conduct networking on-line teaching such as loading, monitoring, modifying, and storing, but also can be used in advanced course such as computer connection, intelligent ASCII peripherals as well.



- A special designed software "WinProladder teaching assistant" can let instructor download or upload ladder program to or from the PLC of the whole class or individual through computer.
- PLC output is isolated by the Relay with socket and fuse and then output to terminal. These isolations can prevent PLC from damaging caused by incorrect wiring and easy for repair and replacement.

Program Development Software

General Features

- Windows based application program following the standard conventions of a windows environment for ease of learning and operation regardless of whether the user is a beginner or frequent user.
- Application environment for project development is via a hierarchical tree. All the elements of the project can be activated by directly clicking the mouse button on the tree object providing comprehensive access and views of the working project.
- Easy entry methods which incorporate both the keyboard and mouse as entry devices. No matter whether on site or in an office environment the software can be operated with ease and efficiency.
- Provides various types of connections to the PLC via a PC. Connections include serial, USB, Ethernet / Internet and Modem. For every different connection WinProladder provides a session name to associate the setting of the communication parameters, such as port no., baud rate, IP address, phone number, etc.

👹 WinProLadder [newspaper.pdw]		
File Edit View Project Ladder PLC Tool Windo	ow Help	
🗋 😂 - 🖶 🛛 🖺 🔛 🎇 🏘 🦷		
■● はなかけま いのののしか さす		
Image: System Configuration Image: System Configuration <	Stadler Diagram: 3- Change Control W20 W24 EN S1 100 D=0- -U/S 0 100 -CY- -SR-	
- ±11 Coin Counter - ±11 Flow Control - ±11 Flow Control - ±11 Flow Control - ±12 Comp Control - ±12 Comp Control - = Table Edit - = Table Edit	W21 M24 EN Stat Page (-) Stat Rt CV Rt CV (Change Contol/ (4) (4) (4) (4)	-
- % ASCII Table - 백 ₁₄ Link Table		
또 Servo Parameter Table 또 Servo Program Table - 약 General Purpose Link Table - 등 Register Table - 양 ModBus Master Tak	X0 X3 X5 EN 51 1 D=0- 507 507 64 -U/S 0 1 0 -	
The Comment The Comment The Comment The Comment The Comment The Comment Status Page	M1 X1 M0 V11 V12 V13 V14 H Sat 10, (s) Sat 10, (s) Sat 10, (s) Sat 10, (s) Sat 10, (s)	
Status monitor Operation Simulate V20 I/O Numbering	-U/S 0 : R4 - CV - -U/S 0 : R4 - CV - - SR -	•
⊟ ■ 0.FBs-40MC(v4.04): DI-V0**223		
DIX0"X23	S Ladder Diagram: 2 - Flow Control	
E-■ 1.8EY :	M20 M10 X9 Y6 W21 H	1
00212M ORG M20 00213M AND M24 00214M LD OPEN 00215M FUN 12 P.(-) Sa: R4		
Sb: R0 D : R8	W20 M10 X9 Y10 H H H H H V00 H11 H H H	
		• •

- On-Line, Run-Time program editing
- Program testing
- Program comments
- Project oriented program
- Ladder program editing screen
- Status monitor and control
- Mnemonic ladder instruction display window
- Ladder diagram with comments
- Element comment editing
- Off-Line Simulation

Instruction Sets

Sequential instructions

Instruction	Operand	Ladder symbol	Function	Instructio	n Operand	Ladder symbol	Function
ORG		┝ ─┤ ├─•	Network starts by an A contact	OR			Parallel connect with an A contact
ORG NOT	х,ү,м,	← / -•	Network starts by a B contact	OR NOT	X,Y,M,		Parallel connect with a B contact
ORG TU	S,T,C	┍─┤↑┝─∙	Network starts by a TU contact	OR TU	S,T,C	Ĩ	Parallel connect with a TU contact
ORG TD		• −−↓↓ −•	Network starts by a TD contact	OR TD			Parallel connect with a TD contact
ORG OPEN		•	Network starts by an open contact	OR OPEN		† †	Parallel connect with an open contact
ORG SHORT		•	Network starts by a short contact	OR SHOP	Г	tt	Parallel connect with a short contact
LD		← ⊢	Branch line starts by an A contact	ANDLD			Concatenate two blocks in series
LD NOT	Х,Ү,М,	+/•	Branch line starts by a B contact	ORLD			Merge two blocks in parallel
LD TU	S,T,C	+ ↑ •	Branch line starts by a TU contact	OUT	NAM C	•()	Output result to coil
LD TD		+ ↓ •	Branch line starts by a TD contact	OUT NO	Y,M,S	• (/)	Output the inverse of result to a coil
LD OPEN		+ •	Branch line starts by an open contact	OUTL	Y	→(L)	Output result to a retentive coil
LD SHORT		•	Branch line starts by a short contact	OUT	TR		Store node status in temporary relay
AND			Serial connect with an A contact	LD			Retrieve node status from temporary relay
AND NOT	X,Y,M,	/ •	Serial connect with a B contact	TU		→ _^	Take differential up of node status
AND TU	S,T,C	→ ↑ →	Serial connect with a TU contact	TD		→ ↓→	Take differential down of node status
AND TD	1	→ ↓ •	Serial connect with a TD contact	NOT		•/•	Inverse node status
AND OPEN		-• •	Serial connect with an open contact	SET		(S)	Set a coil
AND SHORT	1	•	Serial connect with a short contact	RST		(R)	Reset a coil

Step ladder instructions (SFC)

Instruction	Operand	Ladder symbol	Function	Instruction	Operand	Ladder symbol	Function
STP	Snnn	STP-	Define STEP program	ТО	Coop	-то>	STEP divergence
STPEND		- STPEND	STEP program end	FROM	Snnn	FROM	STEP convergence

Function instructions

Category	NO.	Instruction	Derivative	Function	Category	NO.	Instruction	Derivative	Function
Timer		Tnnn		General timer instruction (T0 ~ T255)		200	l→F	DP	Integer to floating point number conversion
Counter		Cnnn		General counter instruction (C0 ~ C255)		201	F→I	DP	Floating point number to integer conversion
Counter	7	UDCTR	D	16 or 32-bit up/down counter		202	FADD	Р	Addition of floating point number
o		SET	DP	Set all bits of register or a discrete point to 1		203	FSUB	Р	Subtraction of floating point number
Setting / Resetting		RST	DP	Clear all bits of register or a discrete point to 0		204	FMUL	Р	Multiplication of floating point number
noootting	114	Z-WR	Р	Zone set or clear		205	FDIV	Р	Division of floating point number
	4	DIFU		Take differential up of the node status to operand		206	FCMP	Р	Comparison of floating point number
Digital operation	5	DIFD		Take differential down of the node status too	Ma	207	FZCP	Р	Zone comparison of floating point number
operation	-			operand	the	208	FSQR	Р	Square root of floating point number
	10	TOGG		Toggle the coil status	mat	209	FSIN	Р	SIN trigonometric function
_	11	(+)	DP	$Sa+Sb \rightarrow D$	ical	210	FCOS	Р	COS trigonometric function
-	12	(-)	DP	$Sa-Sb \rightarrow D$	ope	211	FTAN	Р	TAN trigonometric function
-	13	(×)	DP	$Sa \times Sb \rightarrow D$	Mathematical operation	212	FNEG	Р	Change sign of floating point number
-	14	(/)	DP	$Sa / Sb \rightarrow D$	on	213	FABS	Р	Absolute value of floating point number
_	15	(+1)	DP	Add 1 to D		214	FLN	Р	Floating point napierian logarithm
_	16	(-1)	DP	Subtract 1 from D		215	FEXP	Р	Floating point exponential function
_	23	DIV48	Р	48 bits integer division Sa / Sb \rightarrow D		216	FLOG	P	Floating point logarithm
Mathematica	24	SUM	DP	Sum of N consecutive registers		217	FPOW	P	Floating point power function
athematic	25	MEAN	DP	Average of N consecutive registers		217	FASIN	P	Floating point arc sine function
natio	26	SQRT	DP	Square root of S		210	FACOS	P	Floating point arc cosine function
	27	NEG	DP	Two's complement of D (Negative number)		-		P	Floating point arc tangent function
	28	ABS	DP	Absolute value of D		220	FATAN		01 0
	29	EXT	Р	Extend 16 bits into 32 bits	9	18	AND	DP	Sa AND Sb
	30	PID	Р	PID calculation	Logic	19	OR	DP	Sa OR Sb
	31	CRC16	Р	CRC16 calculation	Logic	35	XOR	DP	Sa XOR Sb
	32	ADCNV		Offset and full scale conversion for analog input	_	36	XNR	DP	Sa XNR Sb
	33	LCNV	Р	Linear conversion	Comparison	17	CMP	DP	Value Compare
	34	MLC	Р	Multiple linear conversion	Comparison	37	ZNCMP	DP	Zone Compare

Instruction Sets

(Continue)

is MOV DP Mores 500 40 HTPO DP More 580 40 Mores 500 40 HTPO DP More 580 40 Mores 500 40 HTPO DP More 580 40 Mores 500 40 HTPO DP More 580 400 Mores 500 40 HTPO DP More 580 400 Mores 500 41 MOV DP More 580 200 Mores 500 42 MOV DP More 580 200 Mores 500 44 MOV DP Mores 500 200 Mores 500 45 MOV P Mores 500 200 Mores 500 46 MOV P Mores 500 200 Mores 500 40 MOV P Mores 500 200 Mores 500 40 MOV P Mores 500 200 Mores 500 Mores 500 200 40 MOV P Mores 500 200	(Continue) Category	NO.	Instruction	Derivative	Function	Category	NO.	Instruction	Derivative	Function
Image: space of the s										
42 BTMV 00 More are black wide to so the 82-bit of 20 43 BVW 00 More are black wide to so the 82-bit of 20 Monitorial of 20 P Rest-and rod plants 44 SVMV 00 More are black wide to so the black wide to so the 92-bit of 20 Monitorial of 20 P Rest-and rod plants 44 SVMV P Seem to High spectra of 20 Monitorial of 20 P Monitorial of 20	Move op	9	MOV/	DP		T	- 07	1.015		
42 BTMV 00 More are black wide to so the 82-bit of 20 43 BVW 00 More are black wide to so the 82-bit of 20 Monitorial of 20 P Rest-and rod plants 44 SVMV 00 More are black wide to so the black wide to so the 92-bit of 20 Monitorial of 20 P Rest-and rod plants 44 SVMV P Seem to High spectra of 20 Monitorial of 20 P Monitorial of 20		40	BITRD	DP	Move the Bit-N of S to FO	mula	88	T.1S		0.1S time base accumulative timer
Image: state		41	BITWR	DP	Write INB input to the Bit-N of D	tive	89	T1S		1S time base accumulative timer
44 NAM 69 Mode the Marker lost of its Marker Mode 44 NAM 69 Mode the Marker lost of its Marker Mode 45 XC166 09 Frankage Raw IDD See Rein X-15 Mer Rein Rein X-15 Mer Rein Rein Rein Rein Rein Rein Rein Rein		42	BITMV	DP	Move the Bit-Ns of S to the Bit -Nd of D	Monitor and	90	WDT	Р	Set watchdon timer
M M V/VV DP Mode mergenes have discussion 44 SVM4 DP Second rest discussion P Amaz 201 45 SVM4 P Second rest discussion P Amaz 201 Amaz 20		43	NBMV	DP	Move the Nibble-Ns of S to the Nibble-Nd of D					•
Image: Probability of Probability of Provide and Probability of Provide and Probability of Probability Probability of Probability Probability of Probability Probability Of Probability Of Probability Of Probab		44	BYMV	DP	Move the Byte-Ns of S to the Byte-Nd of D		-			•
Image: Proceeding of the section of the sectin the section of the section of the section of the section		45	XCHG	DP	Exchange Da and Db	HSC/HST				
Image: Probability of Probability of Provide and Probability of Provide and Probability of Probability Probability of Probability Probability of Probability Probability Of Probability Of Probability Of Probab	erat				Swap the High-Byte of D with the Low-Byte of D				Р	
Image: state	lion							ASCWR		Output ASCII message
So BOIS P Wangs optim muni-light Com- munication Mades optim muni-light 160 WV-FR DP File sight mound-light Com- munication Mades optim muni-light 160 WV-FR DP File sight mound-light Mades optim muni-light Mades optim muni-light 161 WV-FR DP File sight mound-light Mades optim muni-light Mades optim muni-light 162 RD-MP P Rear many mask Mades optim muni-light Mades optim muni-light 163 RD-MP P Rear many mask Mades optim muni-light Mades optim muni-light 163 RD-MP P Rear many mask Mades optim muni-light Mades optim muni-light 163 SPRE DP Statt file of statt file optim muni-light Mades optim muni-light Mades optim muni-light 174 DP Rear many mask Mades optim muni-light Mades optim muni-light Mades optim muni-light 175 DP Rear many mask Mades optim muni-light Mades optim muni-light Mades optim muni-light Mades optim muni-lig		<u> </u>					95	RAMP		Ascending/Descending convenient instruction
Ide RVAP DP Right scame Ide RVAP DP Right scame File CPU In/Concernitation Ide RVAP P Read memory pack File CPU In/Concernitation Ide RVAP P State input to accurate 1 and memory pack File CPU In/Concernitation Ide RVAP P State input to accurate 1 and memory pack File CPU In/Concernitation Ide RVAP P State input to accurate 1 and memory pack File CPU In/Concernitation Ide RVAP P State input to accurate 1 and memory pack File CPU In/Concernitation Ide RVAP P State input to accurate 1 and memory pack File CPU In/Concernitation Ide RVAP P State input to accurate 1 and memory pack File CPU In/Concernitation Ide RVAP P State input to accurate 1 and memory pack File CPU In/Concernitation Ide RVAP P State input to accurate 1 and memory pack File CPU In/Concernitation Ide RVAP P State in and to accurate 1 and memory pack File					-	Descend	98	RAMP2		Tracking type RAMP function for D/A output
Initial Section 2014/1000 With an encorp gark Minite Section 2014/2014 Minite Section 2014/2014 Minite Section 2014/2014 Initial Section 2014/2014 P Read memory gark Minite Section 2014/2014 Minite Section 2014/2014 Minite Section 2014/2014 Initial Section 2014/2014 Section 2014/2014 P Read memory gark Minite Section 2014/2014 Minite Section 2014/2014 Initial Section 2014/2014 Section 2014/2014 Section 2014/2014 Minite Section 2014/2014 Minite Section 2014/2014 Initial Section 2014/2014 Section 2014/2014 Section 2014/2014 Minite Section 2014/2014 Minite Section 2014/2014 Initial Section 2014/2014 D Read Section 1000 P Read Minite Section 2014/2014 Minite Section 2014/2014 Minite Section 2014/2014 Initial Section 2014/2014 D Read Section 1000 P Read Minite Section 2014/2014 Minite Section 2014/2014 Minite Section 2014/2014 Initial Section 2014/2014 D Read Section 1000 Minite Section 2014/2014 Minite Section 2014/2014 Minite Section 2014/2014 Initial Section 2014/2014 D Read Section 2014/2014 Minite Section 2014/2014 Minite Section 2014/2014 Minite Section 2014/2014 Initia						Com-	150	M-BUS		Modbus protocol communication
Inc. Inc. <th< td=""><td></td><td></td><td></td><td></td><td></td><td>munication</td><td>151</td><td>CLINK</td><td></td><td>Fatek CPU link/Generic protocol communication</td></th<>						munication	151	CLINK		Fatek CPU link/Generic protocol communication
6 BSFF DP Shill Dright Tail or bit 1 bit 51 SHR DP Shill Dright Tail or bit 1 bit 53 SHR DP Shill Dright Tail or bit 1 bit 53 SHR DP Shill Dright Tail or bit 1 bit 53 SHR DP Anote DHIN NIS 53 ROTL DP Anote DHIN NIS 54 ROTR DP Convert 5 Nio BCO 55 G				Р			100	R→T	DP	Move register Rs to the table Td
Image: state	(0)				, , , , , , , , , , , , , , , , , , ,		101	T→R	DP	Move the Rp of table Ts to register Rd
Image: Figure	Shift			DP			102	T→T	DP	Move the Rp of table Ts to the Rp of table Td
Image: Figure	/ Rc	52	SHFR	DP	Shift D right N bits		103	BT M	DP	Move table Ts to table Td
Image: Figure	otatio	53	ROTL	DP	Rotate D left N bits					
Proof 57 DECOD P Dacade the N= -N of S 58 ENCOD P Encode the N= -N of S 100 CT_ROT DP Retate table left or right 58 ENCOD P Convert 1-N in No S into 2-segment code DP First in first out (Duce) instruction 61 SSC P Convert 1-N in No S into 2-segment code DP First in first out (Duce) instruction 63 HNS P Convert 1-N in Nuite and second 111 STACK DP First in first out (Duce) instruction 64 ASCI P Convert 1-second by hour, minute and second 121 MAND P AND two matrices 1 MCE Master control loop end 121 MORO P Exclusive NOR (XNR) two matrices 2 SKPE The end of the skp loop 123 MCM P Exclusive NOR (XNR) two matrices 12 MEAK P Subtroafter ownatrices MEM P Subtroafter ownatrices 1 MCE Master control loop start MEM P	n	54	ROTR	DP	Rotate D right N bits					
Proof 57 DECOD P Dacade the N= -N of S 58 ENCOD P Encode the N= -N of S 100 CT_ROT DP Retate table left or right 58 ENCOD P Convert 1-N in No S into 2-segment code DP First in first out (Duce) instruction 61 SSC P Convert 1-N in No S into 2-segment code DP First in first out (Duce) instruction 63 HNS P Convert 1-N in Nuite and second 111 STACK DP First in first out (Duce) instruction 64 ASCI P Convert 1-second by hour, minute and second 121 MAND P AND two matrices 1 MCE Master control loop end 121 MORO P Exclusive NOR (XNR) two matrices 2 SKPE The end of the skp loop 123 MCM P Exclusive NOR (XNR) two matrices 12 MEAK P Subtroafter ownatrices MEM P Subtroafter ownatrices 1 MCE Master control loop start MEM P		20	→BCD	DP	Convert S into BCD	able				
Proof 57 DECOD P Dacade the N= -N of S 58 ENCOD P Encode the N= -N of S 100 CT_ROT DP Retate table left or right 58 ENCOD P Convert 1-N in No S into 2-segment code DP First in first out (Duce) instruction 61 SSC P Convert 1-N in No S into 2-segment code DP First in first out (Duce) instruction 63 HNS P Convert 1-N in Nuite and second 111 STACK DP First in first out (Duce) instruction 64 ASCI P Convert 1-second by hour, minute and second 121 MAND P AND two matrices 1 MCE Master control loop end 121 MORO P Exclusive NOR (XNR) two matrices 2 SKPE The end of the skp loop 123 MCM P Exclusive NOR (XNR) two matrices 12 MEAK P Subtroafter ownatrices MEM P Subtroafter ownatrices 1 MCE Master control loop start MEM P		21	→BIN	DP	Convert S into Binary	op				
Proof 57 DECOD P Dacade the N= -N of S 58 ENCOD P Encode the N= -N of S 100 CT_ROT DP Retate table left or right 58 ENCOD P Convert 1-N in No S into 2-segment code DP First in first out (Duce) instruction 61 SSC P Convert 1-N in No S into 2-segment code DP First in first out (Duce) instruction 63 HNS P Convert 1-N in Nuite and second 111 STACK DP First in first out (Duce) instruction 64 ASCI P Convert 1-second by hour, minute and second 121 MAND P AND two matrices 1 MCE Master control loop end 121 MORO P Exclusive NOR (XNR) two matrices 2 SKPE The end of the skp loop 123 MCM P Exclusive NOR (XNR) two matrices 12 MEAK P Subtroafter ownatrices MEM P Subtroafter ownatrices 1 MCE Master control loop start MEM P		55	B→G	DP	Binary to Gray code conversion	erati	107			Fill Rs into Td table
61 SEC P Convert source to year dy seconds 62 HEX P Convert ASCI code in the bacadecimal 64 HEX P Convert ASCI code in the bacadecimal into ASCII code 64 HEX P Convert ASCI code in the bacadecimal into ASCII code 7 MCC Master control loop start 1 MCC Master control loop start 1 MCC Master control loop start 2 SKP The end of the skip loop 1 The end of the skip loop 2 REAK P Sati from FOR-NEXT loop 2 BEAK P Sati from FOR-NEXT loop 66 JMP P Jump instruction 67 CALL P Call instruction 68 RTS Subroutine of thm instruction 68 RTS Subroutine of thm instruction 69 RT Interrupt eturn instruction 70 FOR The start of the FOR loop 71 NEXT Relum point of FOR loop <td>0</td> <td>56</td> <td>G→B</td> <td></td> <td>Gray code to Binary conversion</td> <td>on</td> <td>108</td> <td>T_SHF</td> <td>DP</td> <td>Shift table left or right</td>	0	56	G→B		Gray code to Binary conversion	on	108	T_SHF	DP	Shift table left or right
61 SEC P Convert source to year dy seconds 62 HEX P Convert ASCI code in the bacadecimal 64 HEX P Convert ASCI code in the bacadecimal into ASCII code 64 HEX P Convert ASCI code in the bacadecimal into ASCII code 7 MCC Master control loop start 1 MCC Master control loop start 1 MCC Master control loop start 2 SKP The end of the skip loop 1 The end of the skip loop 2 REAK P Sati from FOR-NEXT loop 2 BEAK P Sati from FOR-NEXT loop 66 JMP P Jump instruction 67 CALL P Call instruction 68 RTS Subroutine of thm instruction 68 RTS Subroutine of thm instruction 69 RT Interrupt eturn instruction 70 FOR The start of the FOR loop 71 NEXT Relum point of FOR loop <td>ode</td> <td></td> <td></td> <td></td> <td></td> <td></td> <td>109</td> <td>T_ROT</td> <td>DP</td> <td>Rotate table left or right</td>	ode						109	T_ROT	DP	Rotate table left or right
61 SEC P Convert source to year dy seconds 62 HEX P Convert ASCI code in the bacadecimal 64 HEX P Convert ASCI code in the bacadecimal into ASCII code 64 HEX P Convert ASCI code in the bacadecimal into ASCII code 7 MCC Master control loop start 1 MCC Master control loop start 1 MCC Master control loop start 2 SKP The end of the skip loop 1 The end of the skip loop 2 REAK P Sati from FOR-NEXT loop 2 BEAK P Sati from FOR-NEXT loop 66 JMP P Jump instruction 67 CALL P Call instruction 68 RTS Subroutine of thm instruction 68 RTS Subroutine of thm instruction 69 RT Interrupt eturn instruction 70 FOR The start of the FOR loop 71 NEXT Relum point of FOR loop <td>con</td> <td></td> <td></td> <td></td> <td></td> <td></td> <td>110</td> <td>QUEUE</td> <td>DP</td> <td>First in first out (Queue) instruction</td>	con						110	QUEUE	DP	First in first out (Queue) instruction
61 SEC P Convert source to year dy seconds 62 HEX P Convert ASCI code in the bacadecimal 64 HEX P Convert ASCI code in the bacadecimal into ASCII code 64 HEX P Convert ASCI code in the bacadecimal into ASCII code 7 MCC Master control loop start 1 MCC Master control loop start 1 MCC Master control loop start 2 SKP The end of the skip loop 1 The end of the skip loop 2 REAK P Sati from FOR-NEXT loop 2 BEAK P Sati from FOR-NEXT loop 66 JMP P Jump instruction 67 CALL P Call instruction 68 RTS Subroutine of thm instruction 68 RTS Subroutine of thm instruction 69 RT Interrupt eturn instruction 70 FOR The start of the FOR loop 71 NEXT Relum point of FOR loop <td>vers</td> <td></td> <td> </td> <td></td> <td></td> <td></td> <td>111</td> <td>STACK</td> <td>DP</td> <td>First in last out (Stack) instruction</td>	vers						111	STACK	DP	First in last out (Stack) instruction
Image: Figure	ion						112	BKCMP	DP	Compare Rs with zone defined by two tables
Image: Figure							113	SORT	DP	Sort the table
Image: Non-With Section 1000 (Non-With Section 10000 (Non-With Section 10000 (Non-With Section 10000 (Non-With Sec							120	MAND	Р	AND two matrixes
Image: Properties 0 MC Master control loop start 1 MCE Master control loop end 2 SKP The start of the skip loop 3 SKPE The start of the skip loop 2 SKP The start of the skip loop 2 BREAK P Exit more DR-MEXT loop 65 LBL Define the string as label 126 MBRD P Read the bit of a matrix pointed by pointer 67 CALL P Califormation 128 MSHF P Noten that in the oright 1 bit 68 RTS Subroutine return instruction 130 MBROT P Rout the number of bit whose value is 1 or 0 in the matrix 70 FOR The start of the FOR loop 141 MPRA Set VC position parameters 71 NEXF P Retresh I/O immediately Noremenint instruction 142 P							121	MOR	Р	OB two matrixes
I MCE Master control loop end 2 SKP The start of the skip loop 3 SKP The end of the skip loop 3 SKP The end of the skip loop 2 BREAK P Exclusive NOR (XMR) two matrixes 22 BREAK P Exclusive NOR (XMR) two matrixes 22 BREAK P Exclusive NOR (XMR) two matrixes 66 JMP P Submotine return instruction 67 CALL P Call instruction 70 FOR The start of the FOR loop MBCNT P Count the number of bit whose value is 1 or 0 in the matrix 71 NEXT Return point of FOR loop MBCNT P Count the number of bit whose value is 1 or 0 in the matris 72 HKEY <td></td> <td>-</td> <td> </td> <td></td> <td></td> <td></td> <td></td> <td></td> <td></td> <td></td>		-								
Prop 2 SKP The start of the skip loop 3 SKPE The end of the skip loop 124 MINV P Inverse matrix 2 BREAK P Exit from FOR-NEXT loop 125 MCMP P Compare two matrixes and find out the differences between two matrixes 22 BREAK P Exit from FOR-NEXT loop 125 MCMP P Read the bit of a matrix pointed by pointer 65 LBL Define the string as label 66 JMP P Jump instruction 66 JMP P Jump instruction 129 MBSNT P Count the number of bit whose value is 1 or 0 in the matrix 67 CALL P Call instruction 130 MBCNT P Count the number of bit whose value is 1 or 0 in the matrix 68 RTS Subroutine return instruction 140 HSPSO High-speed pulse output 70 FCR The start of the FOR loop 141 MPARA Set NC position parameters 71 NEXEY D 10 keys input convenient instruction										
Proposition END Image: Terminate the execution of program (for debugging) Partial (Windows) Dominates and mindows) Dominates and mindows) 22 BREAK P Exit from FOR-NEXT loop 126 MBRD P Read the bit of a matrix pointed by pointer 65 LBL Define the string as label 127 MBWR P Write the bit of a matrix pointed by pointer 66 JMP P Jump instruction 128 MBSHF P Shift matrix left 1 bit or right 1 bit 67 CALL P Call instruction 1 129 MBROT P Reatematrix left 1 bit or right 1 bit 70 FOR Interrupt return instruction 1 140 HSPSO High-speed pulse output 71 NEXT Return point of FOR loop 1 141 MPARA Set NC position parameters 72 HKEY D 16 keys input convenient instruction 1 142 PSOFF P Force to stop pulse output 78 DSW D 16 keys input convenient instruction 144		2	SKP		The start of the skip loop	M				
Provide END Iterminate me execution of program (reference) Iterminate me execution of program (reference) Iterminate me execution of program (reference) 22 BREAK P Exit from FOR-NEXT loop 126 MBRD P Read the bit of a matrix pointed by pointer 65 LBL Define the string as label 127 MBRD P Write the bit of a matrix pointed by pointer 66 JMP P Jump instruction 128 MBSHF P Shift matrix left 1 bit or right 1 bit 66 RTI Interrupt return instruction 129 MBROT P Rotate matrix left 1 bit or right 1 bit 70 FOR The start of the FOR loop 140 HSPSO High-speed pulse output 71 NEXT Return point of FOR loop 141 MPARA Set NC position parameters 72 TKEY D 10 keys input convenient instruction 142 PSOV P Force to stop pulse output 78 DSW D Thumbwheel switch input convenient instruction 148 MPG Manual pulse generator for positioning </td <td></td> <td>3</td> <td>SKPE</td> <td></td> <td>The end of the skip loop</td> <td>atrix</td> <td>124</td> <td>MINV</td> <td>Р</td> <td></td>		3	SKPE		The end of the skip loop	atrix	124	MINV	Р	
65 LBL Define the string as label 66 JMP P Jump instruction 67 CALL P Call instruction 68 RTS Subroutine return instruction 69 RTI Interrupt return instruction 69 RTI Interrupt return instruction 70 FOR The start of the FOR loop 71 NEXT Return point of FOR loop 71 NEXT Return point of FOR loop 76 TKEY D 10 keys input convenient instruction 77 HKEY D 10 keys input convenient instruction 78 DSW D Trumbwheel switch input convenient instruction 78 DSW D Trumbwheel switch input convenient instruction 79 7SGDL D Trumbwheel switch input convenient instruction 80 MUXI Multiplexing input convenient instruction 81 PLSO D Pulse output(PSO) instruction 81 PLSO D Pulse switch Modulation (PWM) output <			END			ope	125	MCMP	Р	
65 LBL Define the string as label 66 JMP P Jump instruction 67 CALL P Call instruction 68 RTS Subroutine return instruction 69 RTI Interrupt return instruction 69 RTI Interrupt return instruction 70 FOR The start of the FOR loop 71 NEXT Return point of FOR loop 71 NEXT Return point of FOR loop 76 TKEY D 10 keys input convenient instruction 77 HKEY D 10 keys input convenient instruction 78 DSW D Trumbwheel switch input convenient instruction 78 DSW D Trumbwheel switch input convenient instruction 79 7SGDL D Trumbwheel switch input convenient instruction 80 MUXI Multiplexing input convenient instruction 81 PLSO D Pulse output(PSO) instruction 81 PLSO D Pulse switch Modulation (PWM) output <	Flo					ratio	126	MBRD	Р	Read the bit of a matrix pointed by pointer
67 CALL P Call instruction 129 MBROT P Rotate matrix left 1 bit or right 1 bit 68 RTS Subroutine return instruction 30 MBCNT P Count the number of bit whose value is 1 or 0 in the matrix 69 RTI Interrupt return instruction 30 MBCNT P Count the number of bit whose value is 1 or 0 in the matrix 70 FOR Interrupt return instruction 14 MPARA Set NC position parameters 71 NEXT Return point of FOR loop 141 MPARA Set NC position parameters 76 TKEY D 10 keys input convenient instruction 142 PSON P Convert pulse count into mechanical value for display 77 HKEY D 16 keys input convenient instruction 142 MHSPO Multi-Axis high speed pulse output 78 DSW D Thumbwheel switch input convenient instruction 148 MPG Manual pulse generator for positioning 80 MUXI Multiplexing input convenient instruction 116 DIS P Disable extern	N C	<u> </u>		Р		n n	127	MBWR	Р	Write the bit of a matrix pointed by pointer
67 CALL P Call instruction 129 MBROT P Rotate matrix left 1 bit or right 1 bit 68 RTS Subroutine return instruction 30 MBCNT P Count the number of bit whose value is 1 or 0 in the matrix 69 RTI Interrupt return instruction 30 MBCNT P Count the number of bit whose value is 1 or 0 in the matrix 70 FOR Interrupt return instruction 14 MPARA Set NC position parameters 71 NEXT Return point of FOR loop 141 MPARA Set NC position parameters 76 TKEY D 10 keys input convenient instruction 142 PSON P Convert pulse count into mechanical value for display 77 HKEY D 16 keys input convenient instruction 142 MHSPO Multi-Axis high speed pulse output 78 DSW D Thumbwheel switch input convenient instruction 148 MPG Manual pulse generator for positioning 80 MUXI Multiplexing input convenient instruction 116 DIS P Disable extern	ontro			D			128	MBSHF	Р	. , , ,
Image: Submediate of converting of										•
Image: Figure				Г						Ű
Image: Normal Section Processing of the section of the sectin the section of the section of the sectin the section o		<u> </u>							P	
78 DSW D Thumbwheel switch input convenient instruction 79 7SGDL D 7-segment multiplexing display convenient instruction 80 MUXI Multiplexing input convenient instruction 81 PLSO D Pulse output(PSO) instruction 83 SPD Pulse Speed detection instruction 84 TDSP 7/16-segment LED display control 86 TPCTL PID temperature control		<u> </u>				N				• • • •
78 DSW D Thumbwheel switch input convenient instruction 79 7SGDL D 7-segment multiplexing display convenient instruction 80 MUXI Multiplexing input convenient instruction 81 PLSO D Pulse output(PSO) instruction 83 SPD Pulse Speed detection instruction 84 TDSP 7/16-segment LED display control 86 TPCTL PID temperature control						po				
78 DSW D Thumbwheel switch input convenient instruction 79 7SGDL D 7-segment multiplexing display convenient instruction 80 MUXI Multiplexing input convenient instruction 81 PLSO D Pulse output(PSO) instruction 83 SPD Pulse Speed detection instruction 84 TDSP 7/16-segment LED display control 86 TPCTL PID temperature control		74	IMDIO	Р		sitio	142	PSOFF	Р	Force to stop pulse output
78 DSW D Thumbwheel switch input convenient instruction 79 7SGDL D 7-segment multiplexing display convenient instruction 80 MUXI Multiplexing input convenient instruction 81 PLSO D Pulse output(PSO) instruction 83 SPD Pulse Speed detection instruction 84 TDSP 7/16-segment LED display control 86 TPCTL PID temperature control		76		D	,	n co	143	PSCNV	Р	Convert pulse count into mechanical value for display
78 DSW D Thumbwheel switch input convenient instruction 79 7SGDL D 7-segment multiplexing display convenient instruction 80 MUXI Multiplexing input convenient instruction 81 PLSO D Pulse output(PSO) instruction 83 SPD Pulse Speed detection instruction 84 TDSP 7/16-segment LED display control 86 TPCTL PID temperature control		77	HKEY	D	16 keys input convenient instruction	ntro	147	MHSPO		Multi-Axis high speed pulse output
Normalized bit with the second sec		78	DSW	D	Thumbwheel switch input convenient instruction	_	148	MPG		Manual pulse generator for positioning
Improvide Instruction Instruction Instruction 80 MUXI Multiplexing input convenient instruction 146 DIS P Disable external input or peripheral interrupt 81 PLSO D Pulse output(PSO) instruction 170 = D Equal to compare 82 PWM Pulse Width Modulation (PWM) output instruction Pulse speed detection instruction 171 > D Greater than compare 83 SPD Pulse speed detection instruction 172 <		79	75GDI	D		Interrunt	145	EN	Р	Enable external input or peripheral interrupt
80 MUXI Multiplexing input convenient instruction 81 PLSO D Pulse output(PSO) instruction 82 PWM Pulse Width Modulation (PWM) output instruction 83 SPD Pulse speed detection instruction 84 TDSP 7/16-segment LED display control 86 TPCTL PID temperature control	1/0				Instruction		146	DIS	Р	Disable external input or peripheral interrupt
82 PWM Pulse width Modulation (PWM) output instruction Pulse width Modulation (PWM) output instruction Pulse width Modulation (PWM) output 83 SPD Pulse speed detection instruction 84 TDSP 7/16-segment LED display control 86 TPCTL PID temperature control		80	MUXI		Multiplexing input convenient instruction				D	
82 PWM Pulse width Modulation (PWM) output instruction Pulse width Modulation (PWM) output instruction Pulse speed detection instruction 83 SPD Pulse speed detection instruction 84 TDSP 7/16-segment LED display control 86 TPCTL PID temperature control	truc	81	PLSO	D	Pulse output(PSO) instruction					Greater than compare
82 PWM Pulse width Modulation (PWM) output instruction Pulse width Modulation (PWM) output instruction Pulse speed detection instruction 83 SPD Pulse speed detection instruction 84 TDSP 7/16-segment LED display control 86 TPCTL PID temperature control	tion				,	.ine Inst				
86 TPCTL PID temperature control 175 =< D Less than or equal to compare		82	PWM			Com				
86 TPCTL PID temperature control 175 =< D Less than or equal to compare		83	SPD			ions				
86 TPCTL PID temperature control 175 =< D Less than or equal to compare		84	TDSP		7/16-segment LED display control	ison				
139 HSPWM High speed PWM pulse output Other 190 STAT Read system status		86	TPCTL				175	=<	D	
		139	HSPWM		High speed PWM pulse output	Other	190	STAT		Read system status

FATEK[®] The Brand You Can Rely on! Dimensions

Figure 1

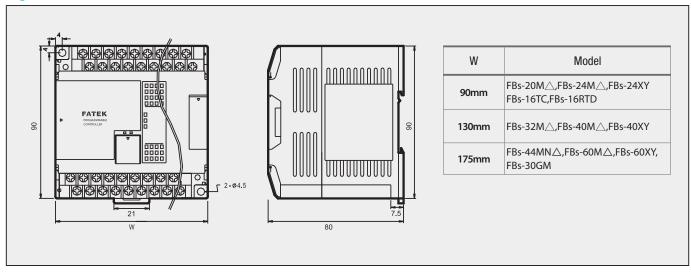


Figure 2

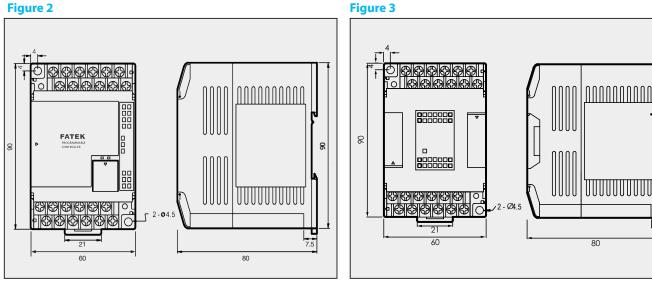


Figure 4

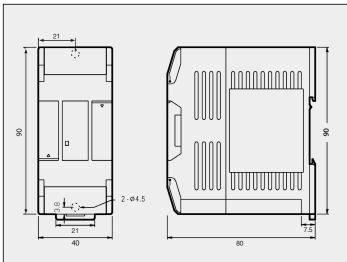
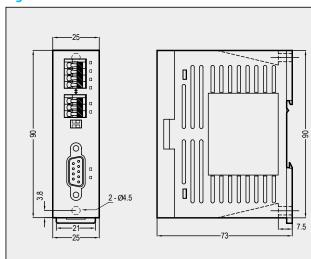


Figure 5



90

7.5

Dimensions

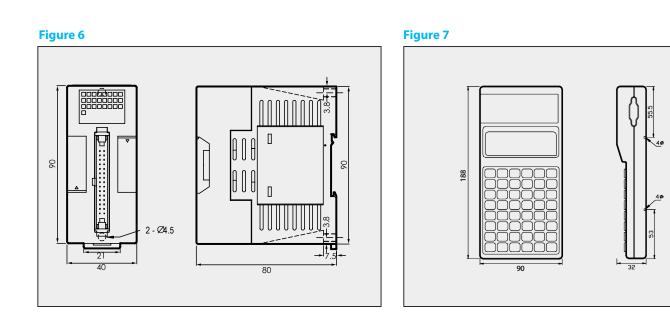


Figure 8

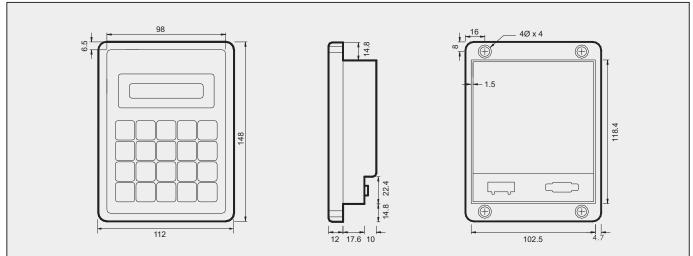
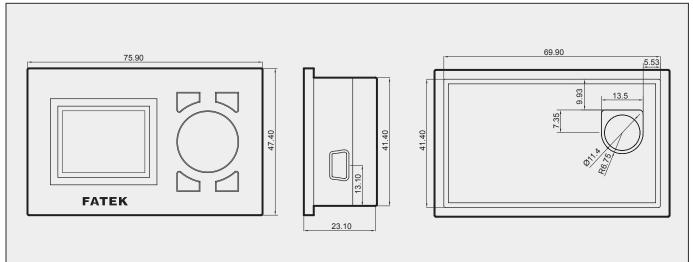


Figure 9



Model List

	Module Nam	A	Specifications
	FBs-10MA⇔∆-©-C		6 points 24VDC digital input (4 points medium speed 20KHz, 2 points medium speed total 5KHz); 4 points relay or transistor output (4 points medium speed 20KHz); 1 RS232 or USB port (expandable up to 3); I/O is not expandable
		FBs-14MA ◇△ - ◎ - C	8 points 24VDC digital input (4 points medium speed 20KHz, 4 points medium speed total 5KHz); 6 points relay or transistor output (6 points medium speed 20KHz); 1 RS232 or USB port (expandable up to 3); I/O is not expandable
		FBs-20MA ◇△ - ◎ - C	12 points 24VDC digital input (6 points medium speed 20KHz, 6 points medium speed total 5KHz); 8 points relay or transistor output (8 points medium speed 20KHz); 1 RS232 or USB port (expandable up to 3)
	Basic Main Units	FBs-24MA ◇△ - ◎ - C	14 points 24VDC digital input (8 points medium speed 20KHz, 6 points medium speed total 5KHz); 10 points relay or transistor output (8 points medium speed 20KHz); 1 RS232 or USB port (expandable up to 3)
	Main Units	FBs-32MA ◇△ - ◎ - C FBs-32MB ◇△ - ◎ - C	20 points 24VDC digital input (8 points medium speed 20KHz, 8 points medium speed total 5KHz); 12 points relay or transistor output (8 points medium speed 20KHz); 1 RS232 or USB port (expandable up to 3); (MB is detachable terminal block)
		FBs-40MA ◇△ - ◎ - C FBs-40MB ◇△ - ◎ - C	24 points 24VDC digital input (8 points medium speed 20KHz, 8 points medium speed total 5KHz); 16 points relay or transistor output (8 points medium speed 20KHz); 1 RS232 or USB port (expandable up to 3); (MB is detachable terminal block)
Main Units		FBs-60MA ◇△ - ◎ - C FBs-60MB ◇△ - ◎ - C	36 points 24VDC digital input (8 points medium speed 20KHz, 8 points medium speed total 5KHz); 24 points relay or transistor output (8 points medium speed 20KHz); 1 RS232 or USB port (expandable up to 3); (MB is detachable terminal block)
		FBs-10MC◇△-◎	6 points 24VDC digital input (2 points high speed 200KHz, 2 points medium speed 20KHz, 2 points medium speed total 5KHz); 4 points relay or transistor output (2 points high speed 200KHz, 2 points medium speed 20KHz); 1 RS232 or USB port (expandable up to 5); built-in RTC; I/ 0 is not expandable
		FBs-14MC◇△-◎	8 points 24VDC digital input (2 points high speed 200KHz, 2 points medium speed 20KHz, 4 points medium speed total 5KHz); 6 points relay or transistor output (2 points high speed 200KHz, 4 points medium speed 20KHz); 1 RS232 or USB port (expandable up to 5); built-in RTC; I/ 0 is not expandable
		FBs-20MC◇△ - ◎	12 points 24VDC digital input (4 points high speed 200KHz, 2 points medium speed 20KHz, 6 points medium speed total 5KHz); 8 points relay or transistor output (4 points high speed 200KHz, 4 points medium speed 20KHz); 1 RS232 or USB port (expandable up to 5); built-in RTC; detachable terminal block
	Advanced Main Units	FBs-24MC◇△-◎	14 points 24VDC digital input (4 points high speed 200KHz, 4 points medium speed 20KHz, 6 points medium speed total 5KHz); 10 points relay or transistor output (4 points high speed 200KHz, 4 points medium sped 20KHz); 1 RS232 or USB port (expandable up to 5); built-in RTC; detachable terminal block
		FBs-32MC◇△-◎	20 points 24VDC digital input (6 points high speed 200KHz, 2 points medium speed 20KHz, 8 points medium speed total 5KHz); 12 points relay or transistor output (6 points high speed 200KHz, 2 points medium speed 20KHz); 1 RS232 or USB port (expandable up to 5); built-in RTC; detachable terminal block
		FBs-40MC ◇△ - ◎	24 points 24VDC digital input (6 points high speed 200KHz, 2 points medium speed 20KHz, 8 points medium speed total 5KHz); 16 points relay or transistor output (6 points high speed 200KHz, 2 points medium speed 20KHz); 1 RS232 or USB port (expandable up to 5); built-in RTC; detachable terminal block
		FBs-60MC◇△-◎	36 points 24VDC digital input (8 points high speed 200KHz, 8 points medium speed total 5KHz); 24 points relay or transistor output (8 points high speed 200KHz); 1 RS232 or USB port (expandable up to 5); built-in RTC; detachable terminal block
		FBs-20MN◇△ - ◎	2 sets (1 axis) 920KHz 5VDC digital differential input, 10 points 24VDC digital input (4 points high speed 200KHz, 6 points medium speed total 5KHz); 2 sets (1 axis) 920KHz 5VDC digital differential output, 6 points relay or transistor output (average high speed 200KHz); 1 RS232 or USB port (expandable up to 5); built-in RTC; detachable terminal block
	NC Positioning Main Units	FBs-32MN◇△ - ◎	4 sets (2 axes) 920KHz 5VDC digital differential input, 16 points 24VDC digital input (4 points high speed 200KHz, 8 points medium speed total 5KHz); 4 sets (2 axes) 920KHz 5VDC digital differential output, 8 points relay or transistor output (4 points high speed 200KHz); 1 RS232 or USB port (expandable up to 5); built-in RTC; detachable terminal block
		FBs-44MN◇△ - ◎	8 sets (4 axes) 920KHz 5VDC digital differential input, 20 points 24VDC digital input (8 points medium speed total 5KHz); 8 sets (4 axes) 920KHz 5VDC digital differential output, 8 points relay or low speed transistor output; 1 RS232 or USB port (expandable up to 5); built-in RTC; detachable terminal block
	Expansion Power Supply	FBs-EPW-AC/D24	Power supply of 100~240VAC or 24VDC input for expansion module; 3 sets output power with 5VDC, 24VDC, and 24VDC, 14W capacity
		FBs-24XY◇ - 〇	14 points 24VDC digital input, 10 points relay or transistor output, built-in power supply
	DIO	FBs-40XY(>- (0)	24 points 24VDC digital input, 16 points relay or transistor output, built-in power supply
	Expansion Units	FBs-60XY◇ - ◎	36 points 24VDC digital input, 24 points relay or transistor output, built-in power supply
		FBs-8X	8 points 24 VDC digital input
		FBs-8Y	8 points relay or transistor output
		FBs-8XY	4 points 24VDC digital input, 4 points relay or transistor output
		FBs-16Y	16 points relay or transistor output
		FBs-16XY◇	8 points 24VDC digital input, 8 points relay or transistor output
-	DIO Expansion Modules	FBs-20X	20 points 24VDC digital input
Righ		FBs-24XY◇	14 points 24VDC digital input, 10 points relay or transistor output
It S		FBs-40XY🛇	24 points 24VDC digital input, 16 points relay or transistor output
ide		FBs-60XY	36 points 24VDD digital input, 24 points relay or transistor output
Exp		FBs-24X	24 points high-density 24VDC digital input, 30 pins header with latch
ans		FBs-24YT/J	24 points high-density transistor SINK(T) or SOURCE(J) output (0.1A max.), 30 pins header with latch
Right Side Expansion Modules	Thumbwheel Switch Module	FBs-32DGI	8 sets 4 digits (total 32 digits) thumbwheel switch (or 128 points independent switch) multiplex input module, 30 pins header connector
Mc			1 set 8 digits (total 32 digits) multiplication (or 122 points independent switch) multiplication and the provided connector
)dul	16/7 Segment LED Display Modules	FBs-7SG1	
es	iviouules	FBs-7SG2	2 sets 8 digits 7-segment/4 digits 16-segment LED display (or 128 points independent LED) output display module, 16 pins header connector
		FBs-2DA	2 channels, 14-bit analog output module (-10~10V, 0~10V or -20~20mA, 0~20mA)
	AIO Modules	FBs-4DA	4 channels, 14-bit analog output module (-10~10V, 0~10V or -20~20mA, 0~20mA)
		FBs-4A2D	4 channels, 14-bit analog input (same specification as 6AD)+2 channels, 14-bit analog output (same specification as 2DA) combo module
		FBs-6AD	6 channels, 14-bit analog input module (-10~10V, 0~10V or -20~20mA, 0~20mA)
		FBs-2TC	2 channels, thermocouple temperature input module with 0.1°C resolution.
		FBs-6TC	6 channels, thermocouple temperature input models with 0.1°C resolution.
	Temperature	FBs-16TC	
	Measurement		16 channels, thermocouple temperature input module with 0.1°C resolution.
	Modules	FBs-6RTD	6 channels, RTD temperature input module with 0.1°C resolution.
		FBs-16RTD	16 channels, RTD temperature input module with 0.1°C resolution.
		FBs-6NTC	6 channels, NTC temperature input module with 0.1°C resolution.

Model List

	ue) Module Name		Specifications			
R		FBs-2A4TC	2 channels, 14-bit analog input (same specifications as 6AD)+ 4 channels thermocouple temperature input (same specifications as 6TC) combo module			
Right Side Expansion Modules	AI + Temperature Measurement Combo Modules	FBs-2A4RTD	2 channels, 14-bit analog input (same specifications as 6AD) + 4 channels RTD temperature input (same specifications as 6RTD) combo module			
	Voice Modules	FBs-VOM	Built-in 1MB memory (play continuously up to 2 minutes), extendable 4GB SD card(play continuously up to 8,000 minutes) voice module, 245 messages, output 2W			
Insi	Load Cell Module	FBs-1LC	1 channel, load cell measurement module with 16-bit resolution (including sign bit)			
on	Potential Meter Module	FBs-4PT	4 channels, 14-bit potential meter input module (Impedance range: $1 \sim 10 \text{K} \Omega$)			
		FBs-CM22	2 ports RS232 (Port3 +Port 4) communication module			
		FBs-CM55	2 ports RS485 (Port3 + Port 4) communication module			
		FBs-CM25	1 port RS232 (Port3) + 1 port RS485 (port 4) communication module			
		FBs-CM25E	1 port RS232 (Port3) + 1 port RS485 (port 4) + Ethernet network interface communication module			
		FBs-CM55E	1 port RS485 (Port3) + 1 port RS485 (port 4) + Ethernet network interface communication module			
	Communication	FBs-CMZB	ZigBee communication module			
	Modules	FBs-CMZBR	ZigBee communication repeater			
		FBs-CMGSM	GSM wireless communication module			
		FBs-CM25C	General purpose RS232 to RS485/RS422 communication interface converter with photocouple isolation			
		FBs-CM5R	General purpose RS485 repeater with photocouple isolation			
		FBs-CM5H	General purpose 4 ports RS485 HUB with photocouple isolation, RS485 can be connected as star connection			
_		FBs-CB2	1 port RS232 (Port 2) communication board			
.eft (FBs-CB22	2 ports RS232 (Port 1+ Port 2) communication board			
Left Side Expansion Modules		FBs-CB5	1 port RS485 (Port 2) communication board			
Exp	Communication	FBs-CB55	2 ports RS485 (Port 1+ Port 2) communication board			
Dans	Boards	FBs-CB25	1 port RS232 (Port 1) + 1 port RS485 (Port 2) communication board			
ion		FBs-CBE	1 port 10 Base T Ethernet communication board			
Moc		FBs-CBEH	1 port 100 Base T Ethernet communication board			
lules		FBs-CBCAN	1 port CANopen communication board			
00		FBs-B2DA	2 channels, 12-bit analog output board (0~10V or 0~20mA)			
	AIO	FBs-B2A1D	2 channels, 12-bit analog input + 1 channel, 12-bit analog output combo analog board (0~10V or 0~20mA)			
	Boards	FBs-B4AD	4 channels, 12-bit analog input board (0~10V or 0~20mA)			
	Precision Load Cell Module	FBs-1HLC	1 channel, high precision weighing control module with 24-bit resolution			
	3-Axis Motion Control Module	FBs-30GM	3-Axis with linear and circular interpolation advanced motional control module, 3 sets of 200KHz high speed pulse input, 3 sets of 500KHz high speed pulse output, 14 points main unit, 16M Bytes program capacity, 20K Words retentive file register, built-in RS485 and Ethernet, 7.62mm detachable terminal block			
		FBs-BDAP	Board type Data Access Panel			
		FBs-BPEP	Board type Parameter Entry Panel			
	Simple HMI	FBs-PEP/PEPR	Multi characters with graphics-based Parameter Entry Panel, built-in RFID Read/Write module with PEPR			
		FBs-DAP-B/BR	16 X 2 LCD character display, 20 keys keyboard, 24VDC power supply, RS485 comm. port, built-in RFID Read/Write module with BR			
		FBs-DAP-C/CR	16 X 2 LCD character display, 20 keys keyboard, 5VDC power supply, RS232 comm. port, built-in RFID Read/Write module with CR			
	RFID Card	CARD-H	Read / Write wireless card (for FBs-DAP-BR/CR and FBs-PEPR)			
		FP-08	FBs- Series PLC handheld programmer			
	Programming Devices	Winproladder	FATEK-PLC Winproladder Programming software			
	Memory Pack	FBs-PACK	FBs-PLC program memory pack with 20K Words program, 20K Words register, write protection switch			
	PWMDA Module	PWMDA	10-bit single channel pulse width modulation(PWM) 0~10V analog output (AO) module			
-	USB- RS232 Converter Cable	FBs-U2C-MD-180	Communication converter cable with standard USB AM connector to RS232 MD4M connector (used in standard PC USB to FBs mai unit Port 0 RS232), length 180cm			
^v erip		FBs-232P0-9F-150	MD4M to DB9F communication cable (FBs main unit Port 0 RS232 connect to standard DB9M), length 150cm			
Peripheral and Accessory	Communication 0.11	FBs-232P0-9M-400	MD4M to DB9M communication cable (FBs main unit Port 0 RS232 connect to DB9F), length 400cm			
	Communication Cables	FBs-232P0-MD-200	MD4M to MD4M communication cable (FBs main unit Port 0 RS232 connect to FBs-PEP/PEPR), length 200cm			
		FBs-232P0-MDR-200	MD4M to 90° MD4M communication cable (FBs main unit Port 0 RS232 connect to FBs-PEP/PEPR), length 200cm			
	High Density DIO Connection Cable	HD30-22AWG-200	High density modules(FBs-24X, FBs-24YT/J, FBs-32DGI) connector 30pin Socket, 22AWG I/O cable length200cm			
		DBAN.8-nR	0.8" 4-digit 16-segment LED display, n means R(Red) 16-segment LED characters display installed, can be 1~4			
		DBAN.2.3-nR	2.3" 4-digit 16-segment LED display, n means R(Red) 16-segment LED characters display installed, can be 1~4			
	16/7-Segment	DB.56-nR	0.56" 8-digit 7-segment display, n means R(Red) 7-segment LED characters display installed, can be 1~8			
	LED Display	DB.8-nR	0.8" 8-digit 7-segment display, n means R(Red) 7-segment LED characters display installed, can be 1~8			
		DB2.3-nR	2.3" 8-digit 7-segment display, n means R(Red) 7-segment LED characters display installed, can be 1~8			
		DB4.0-nR	4.0" 4-digit 7-segment display, n means R(Red) 7-segment LED characters display installed, can be 1~4			
	Training Box	FBs-TBOX	46cm x 32 cm x 16cm suitcase, containing FBs-24MCT main unit. FBs-CM25E communication module (RS232 + RS485 + Ethernet network), 14 simulated input switches, 10 external relay output, Doctor terminal outlet I/O, peripherals such as stepping motor,			

 1. <>: R — Relay output; T — Transistor SINK(NPN) output J — Transistor SOURCE (PNP) output
 2. △: 2 — built-in RS232 port; U — built-in USB port (non-standard) 3. © : AC — 100~240VAC power supply D12 — 12VDC power supply D24 — 24VDC power supply 4.-C : Blank — Standard ; -C — add in RTC

5. The unmarked frequencies of Digital Input (DI) or Digital Output (DO) are low speed. FATEK[®] The Brand You Can Rely on!



FATEK[®] AUTOMATION CORPORATION

26FL., NO. 29, SEC. 2, JUNGJENG E. RD., DANSHUEI DIST., NEW TAIPEI CITY 25170, TAIWAN, R.O.C

TEL : +886-2-2808-2192

FAX : +886-2-2809-2618

E-mail : sales@fatek.com tech@fatek.com

Website : www.fatek.com